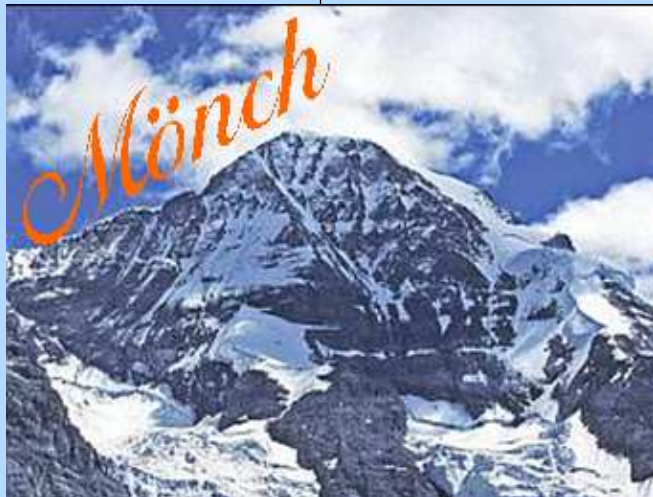


PAUL SCHERRER INSTITUT



Wir schaffen Wissen – heute für morgen



Paul Scherrer Institut

Roberto Dinapoli

roberto.dinapoli@psi.ch

Charge integration detectors for X-ray applications

Single photon counting detectors for X-ray applications

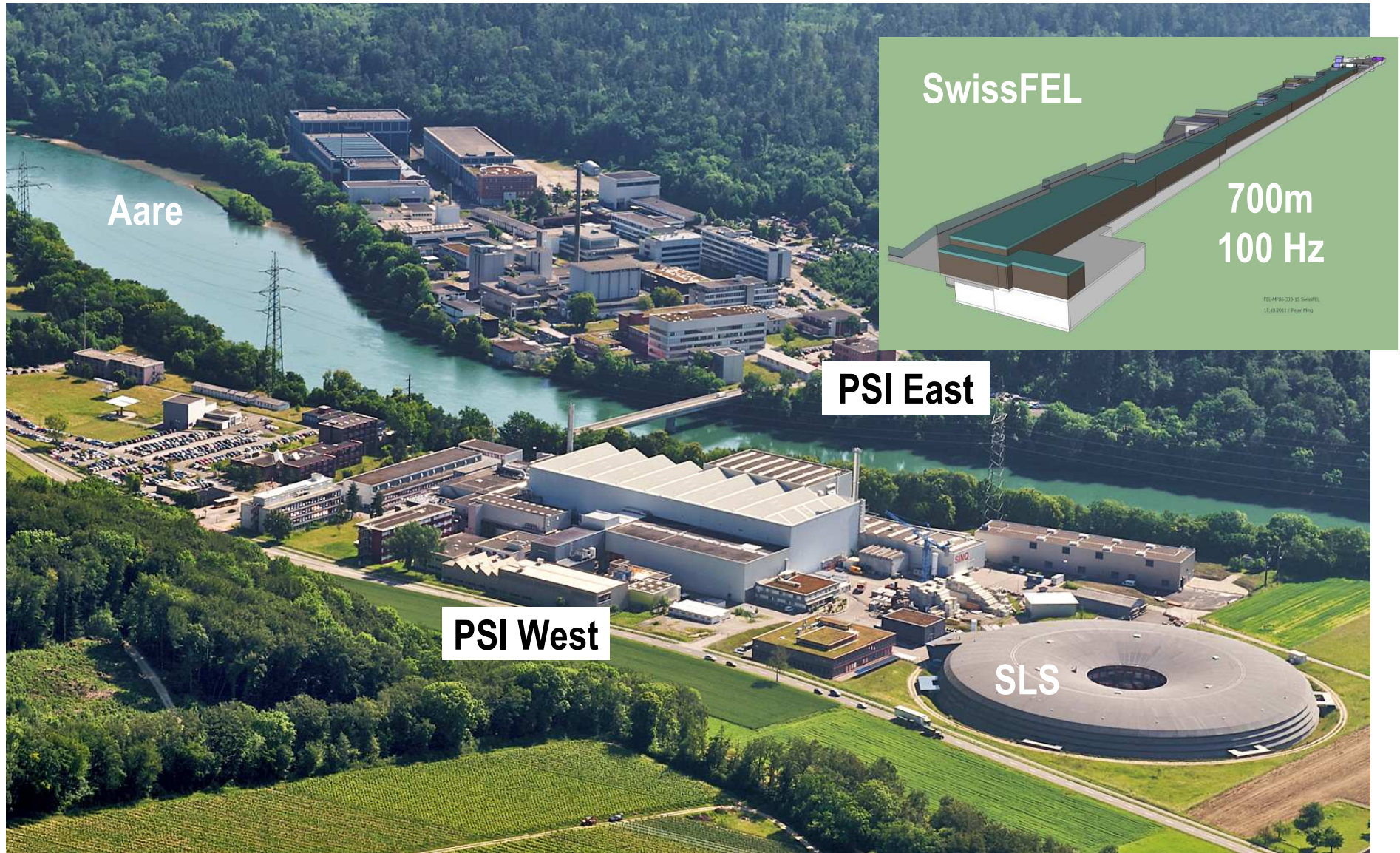
Yesterday

- Introduction of Paul Scherrer Institut
- Chip design at PSI
- Basics of X-ray synchrotron radiation emission
- Detectors developed by the SLS Detector group for X-ray detection
 - Single photon counting detectors
 1. PILATUS (2D)
 2. MYTHEN (1D)
 3. EIGER (2D)

Single photon counting detectors for X-ray applications

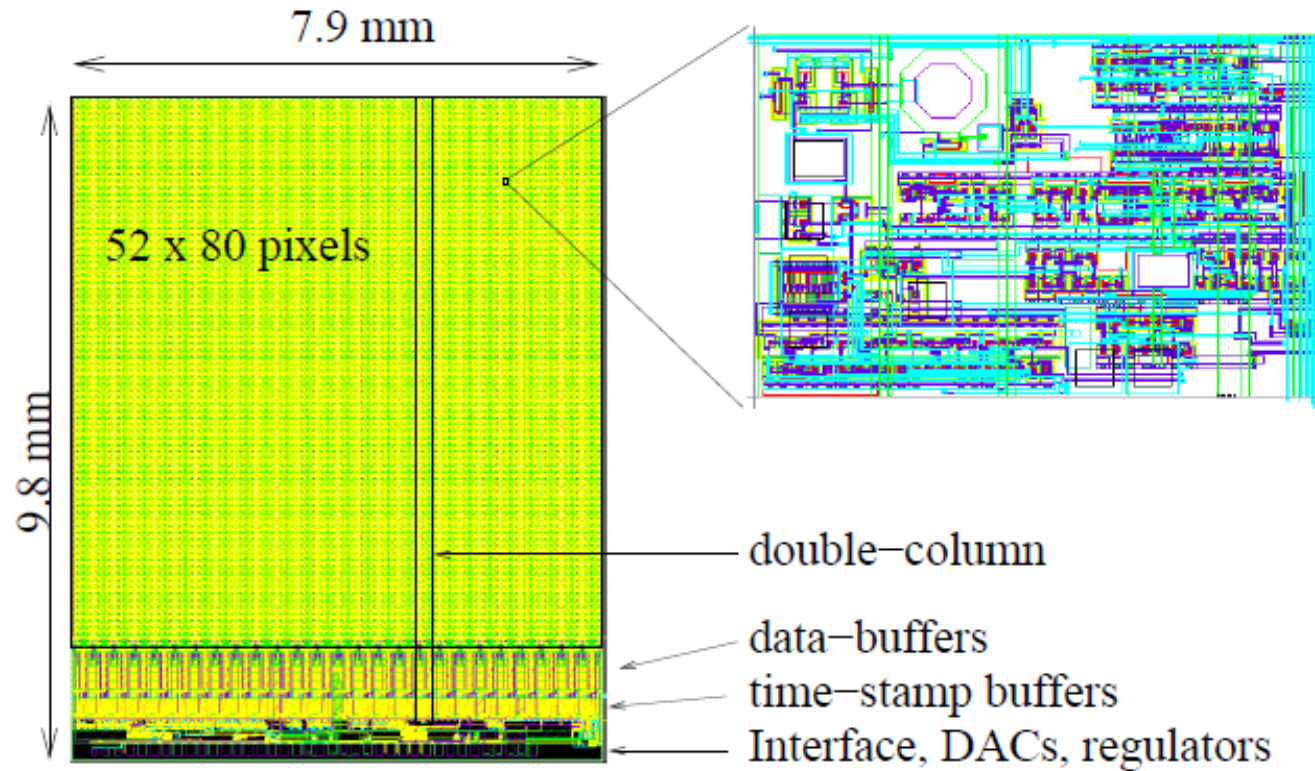
- Summary of the single photon counting detector talk
- Basic of charge integration with analog readout
 - Charge integration detectors
 1. AGIPD (2D, XFEL)
 2. GOTTHARD (1D, XFEL & Synchrotron)
 3. JUNGFR AU (2D, XFEL & Synchrotron)
 4. MÖNCH (2D, XFEL & Synchrotron)
- MÖNCH

PAUL SCHERRER INSTITUT (2011)



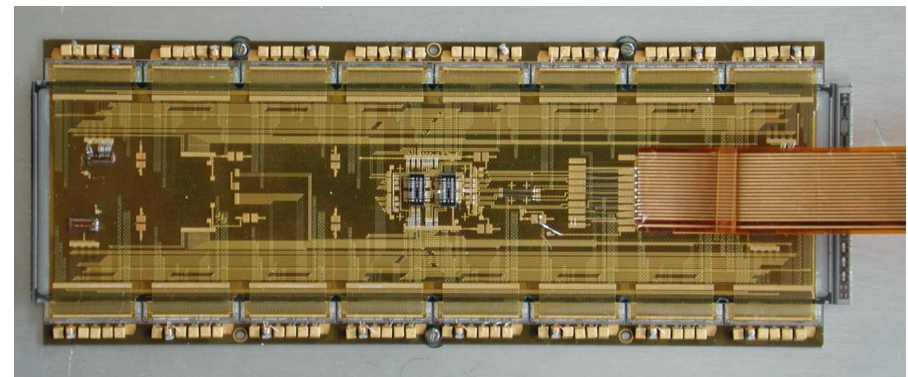
~1700 Staff employees; 30Km from Zurich

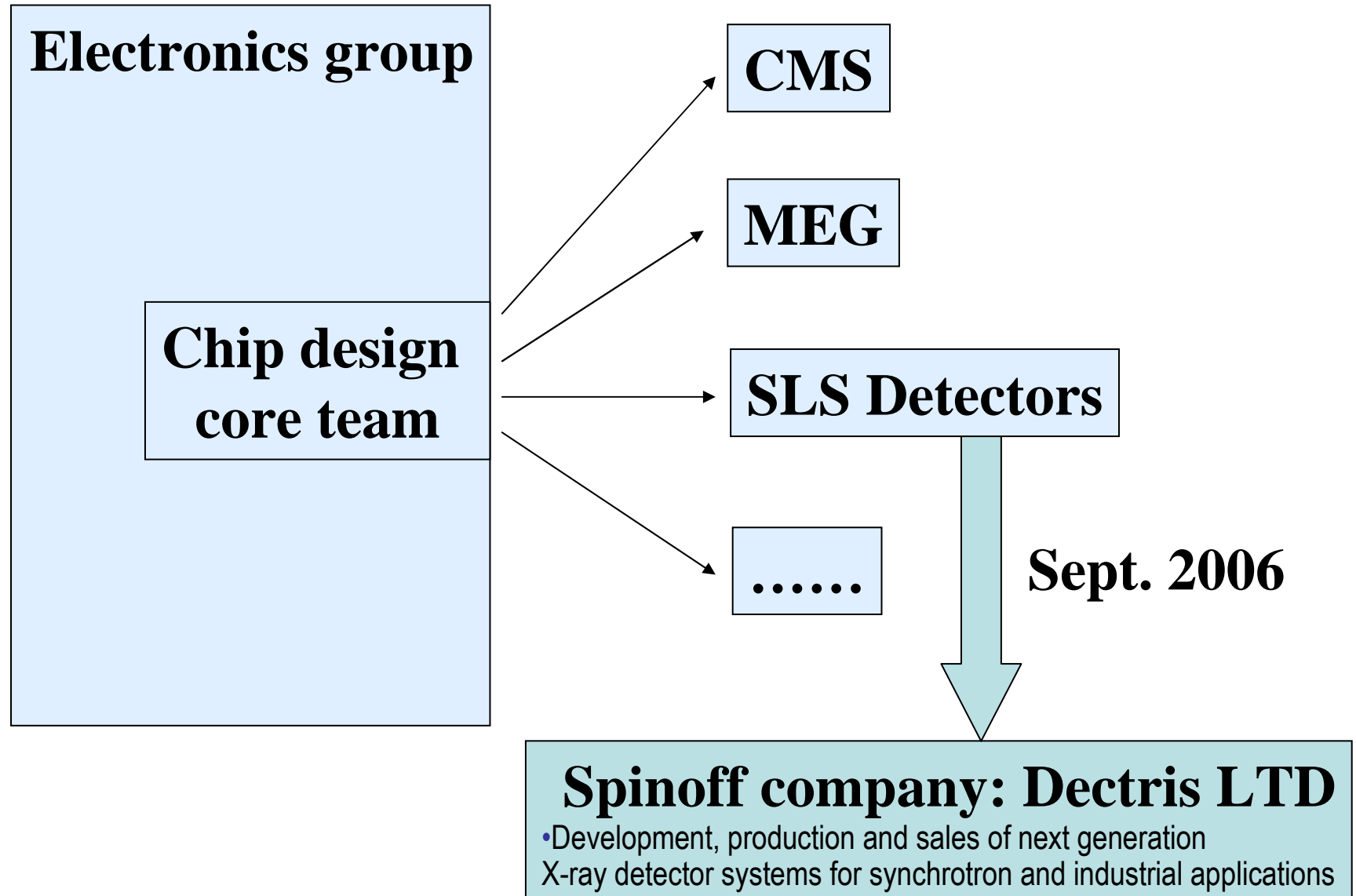
The CMS Pixel Chip (Roland Horisberger)



- Technology: 0.25 um IBM
- Pixel size 150x100 um²
- Tested up to 100MHz/cm² ~ 20 kHz/pixel
- Time resolution: 25 ns
- Deadtime/ pixel: 100ns
- Module: 416 x 160 pixels (62.4 x 16 mm²)

CMS module based on PSI46 Chip





Particle physics

CMS

Pixel chip for the inner tracker
New version for LHC luminosity upgrade

MEG

Domino sampling chips (DRS4)
New version for MEEE (DRS5)

X-ray detection

Synchrotron light (Mythen, Pilatus, Eiger)

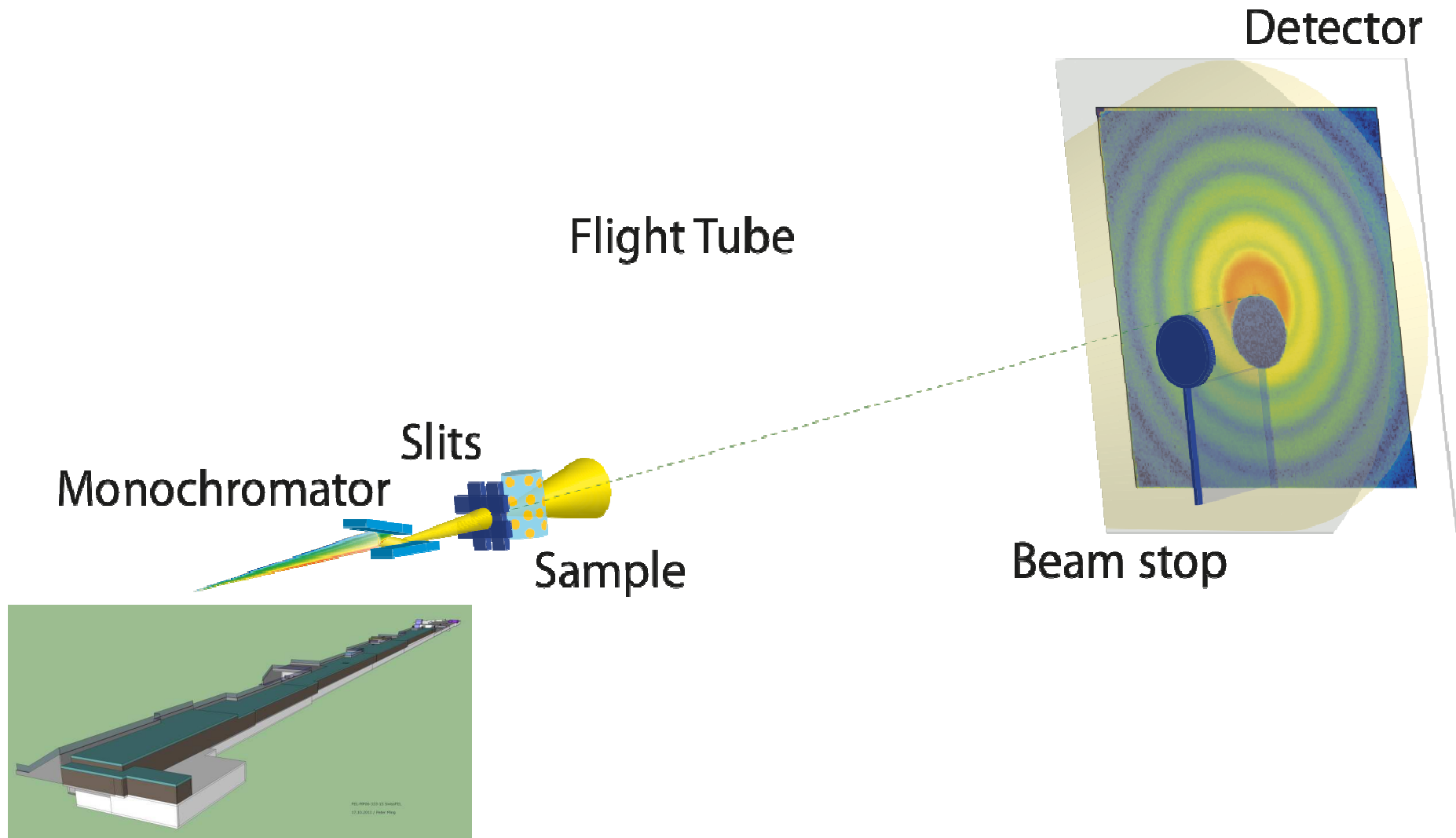
XFEL light (Agipd, Gotthard, Jungfrau, Mönch)

Astronomy

Sensor design

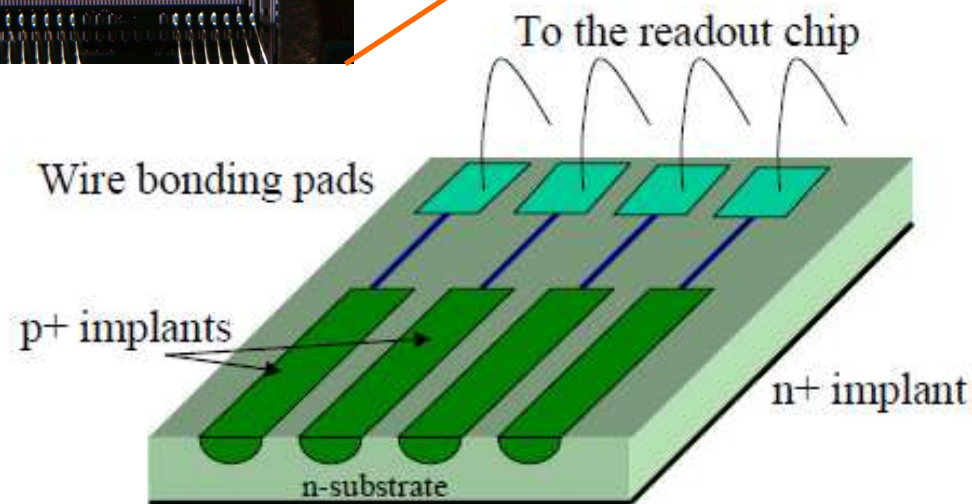
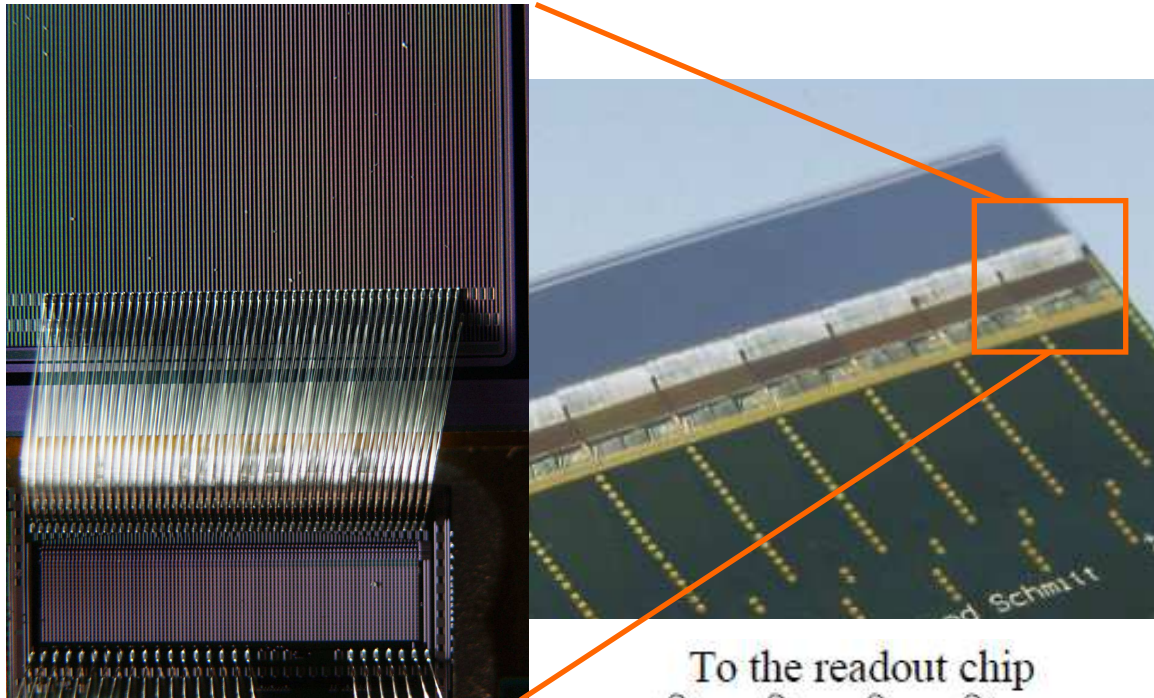
Research

HiZ – Neutrons – Low energy X-ray – Electrons
Detectors



- Specs definition
 - Chip design CAD maintenance (HW and SW)
 - ASIC design
 - Sensor design
 - Bump-bonding masks design
 - Bump bonding
 - Wirebonding
 - Design of the ASIC test system (HW, firmware, SW)
 - ASIC tests
- Full readout system (HW, firmware, SW)
 - Mechanics and cooling design (and partly production)
 - Data transfer to long term storage
 - Design of the wafer testing system
 - Detector assembly
 - Detector tests
 - Detector commissioning
 - Detector „mass“ production
 - Detector support

1D hybrid detectors (strips)



Strip detectors

- High segmentation
- Small data throughput
- Lower cost per area

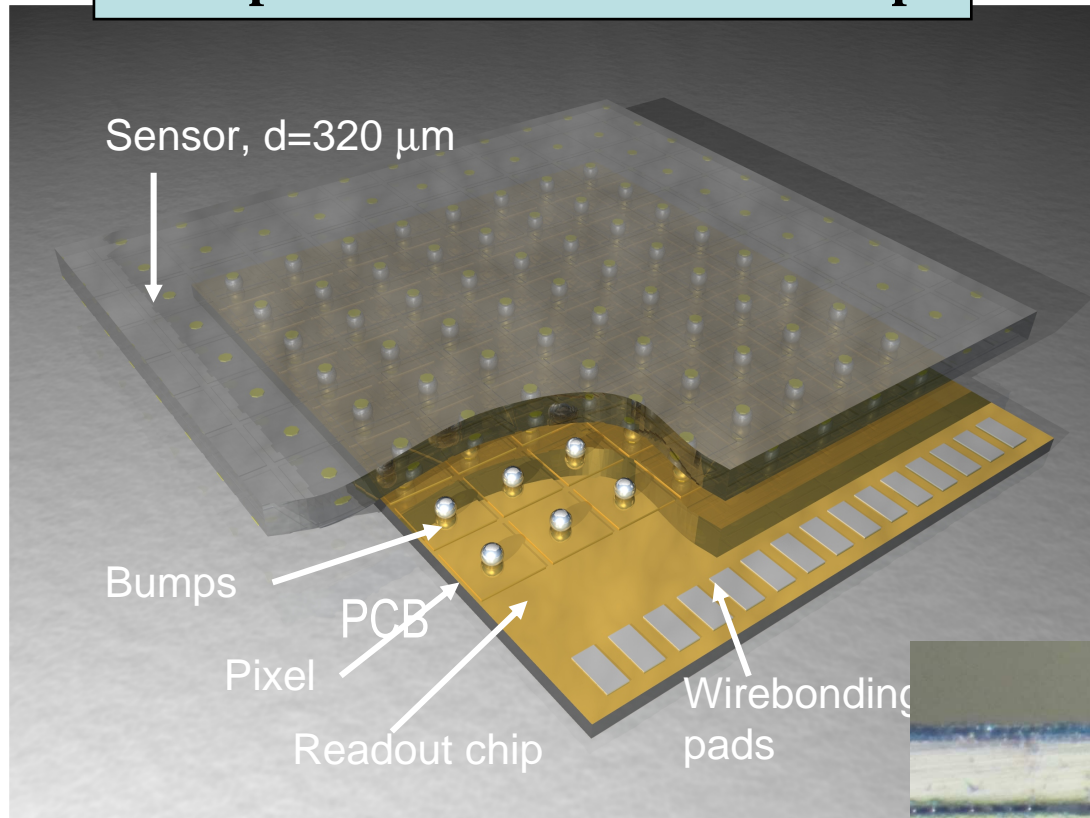
Powder diffraction

Fluorescence emission spectrometers

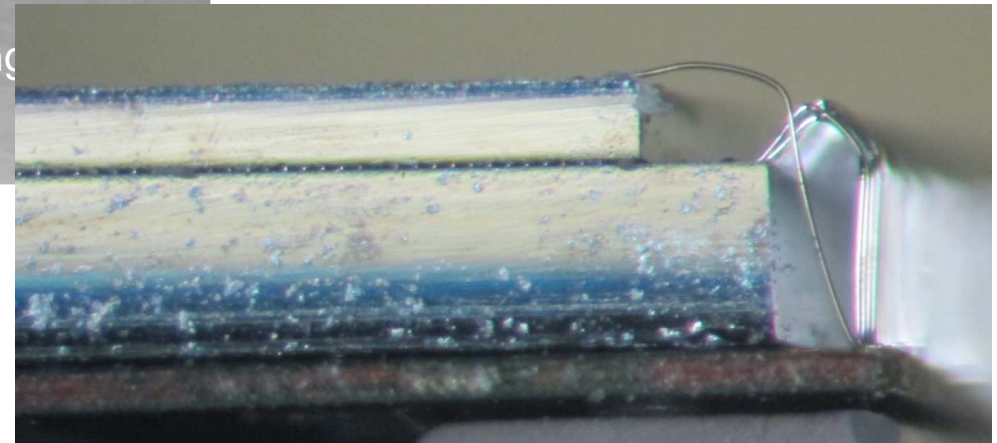
Beam position monitors

2D hybrid detectors (pixels)

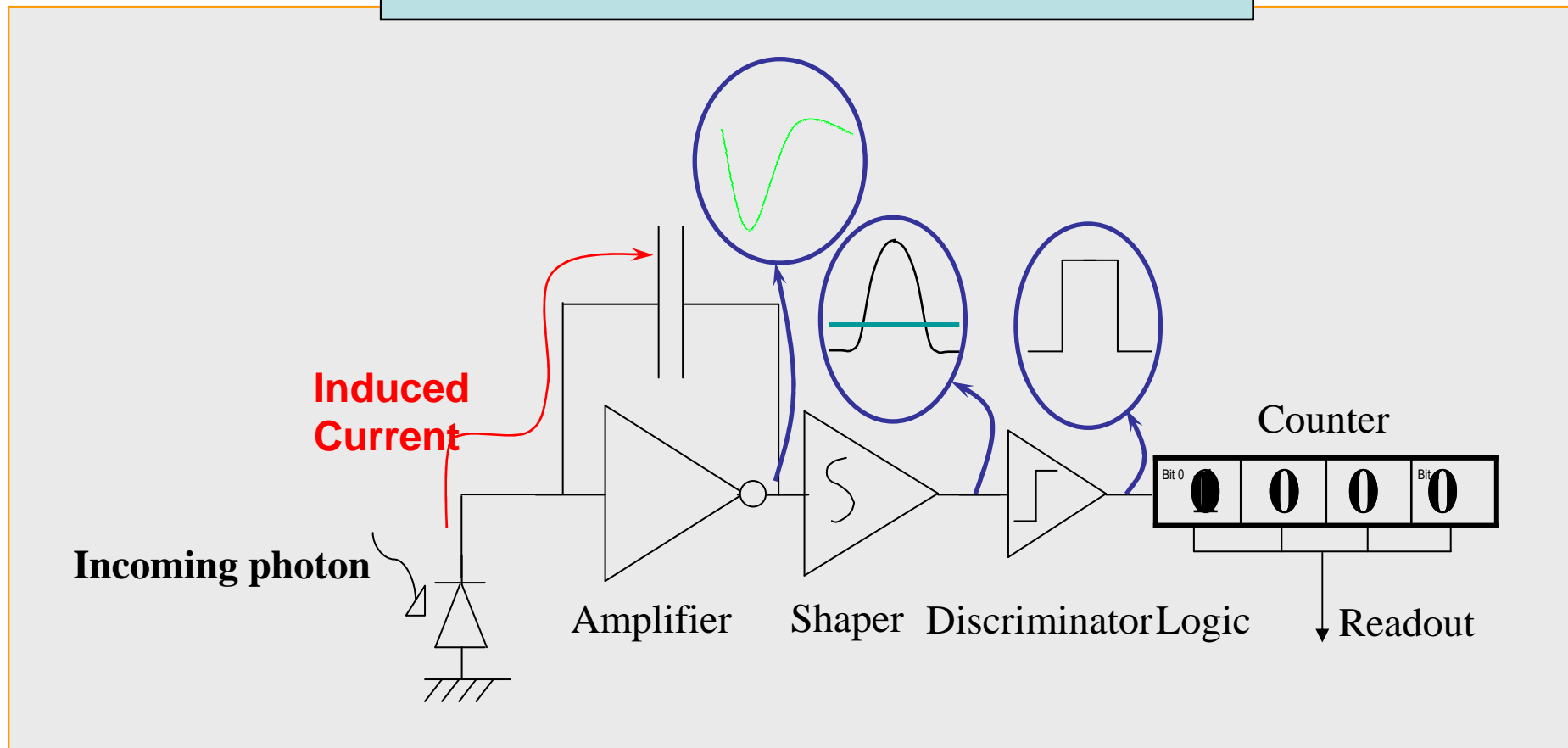
**A silicon detector
bump-bonded with a readout chip**



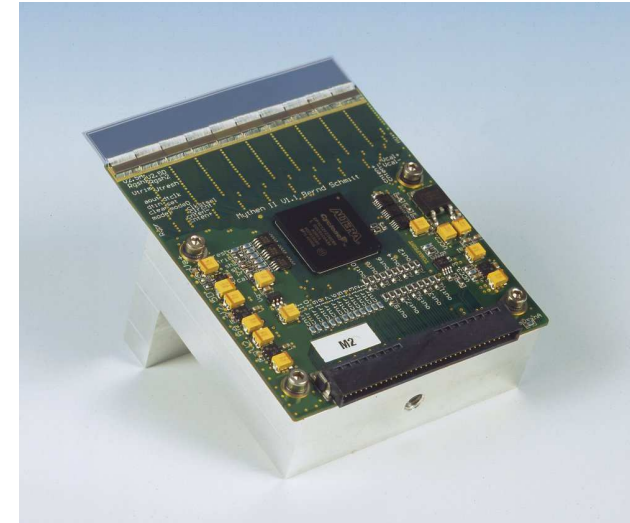
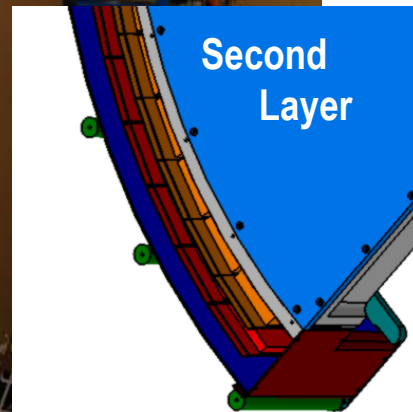
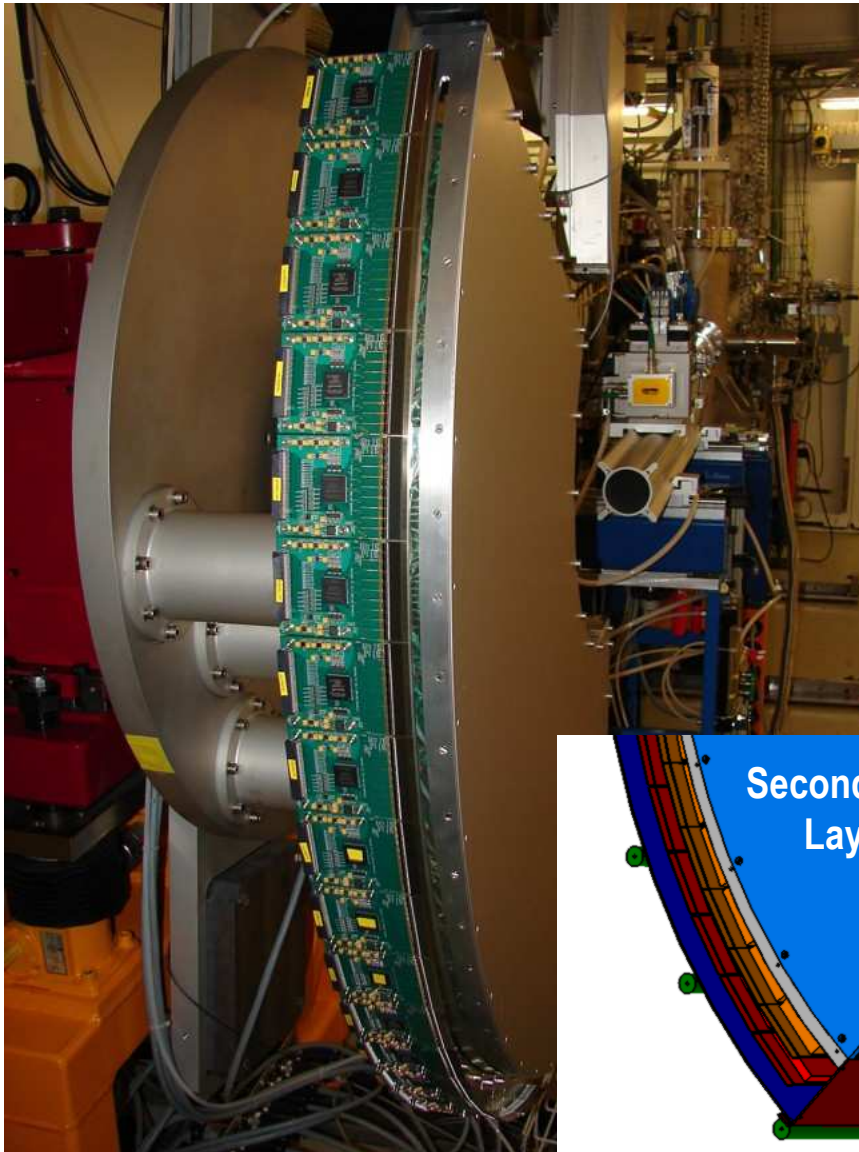
2D information!



Simplified working scheme of a channel



Mythen II full detector



- full 120° spectrum taken in less than a second
- makes measurements 5000-15000 times faster
- solves problem of radiation damage in organics
- **unique tool for time resolved 1D experiments (powder diffraction)**

120° Mythen system at the Powder diffraction end station, MS beamline

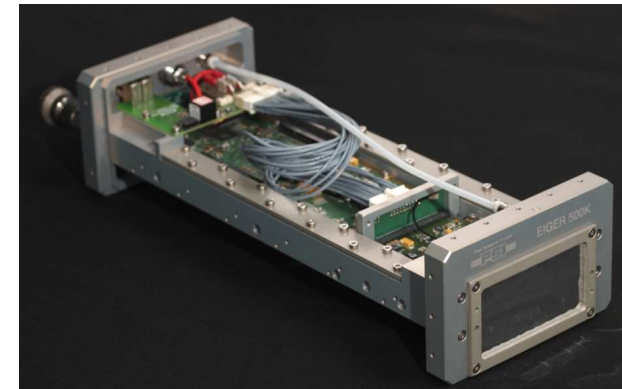
PILATUS I



PILATUS II



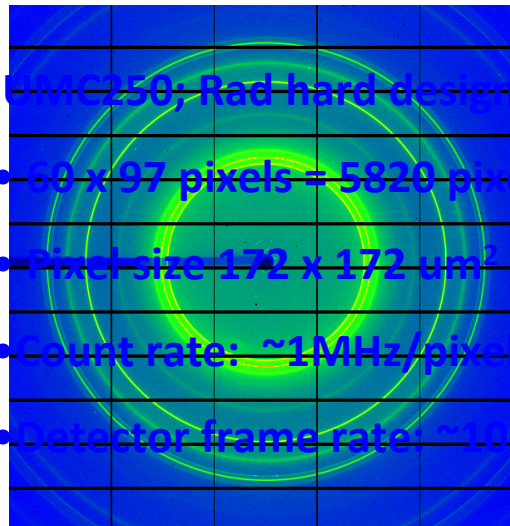
EIGER



- DMILL rad-hard technology
- Pixel size: $217 \times 217 \mu\text{m}^2$
- Chip pixel array: 44×78
- Count rates $< 10 \text{ kHz/pixel}$
- Det. Frame rate: 2 Hz

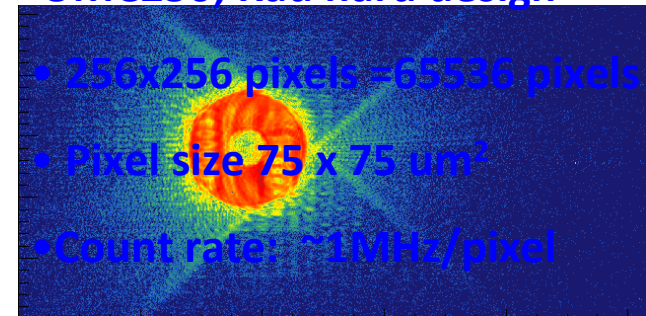


- UMC250; Rad hard design
- 60×97 pixels – 5820 pixels
- Pixel size $172 \times 172 \mu\text{m}^2$
- Count rate: $\sim 1 \text{ MHz/pixel}$
- Detector frame rate: $\sim 10 \text{ Hz}$

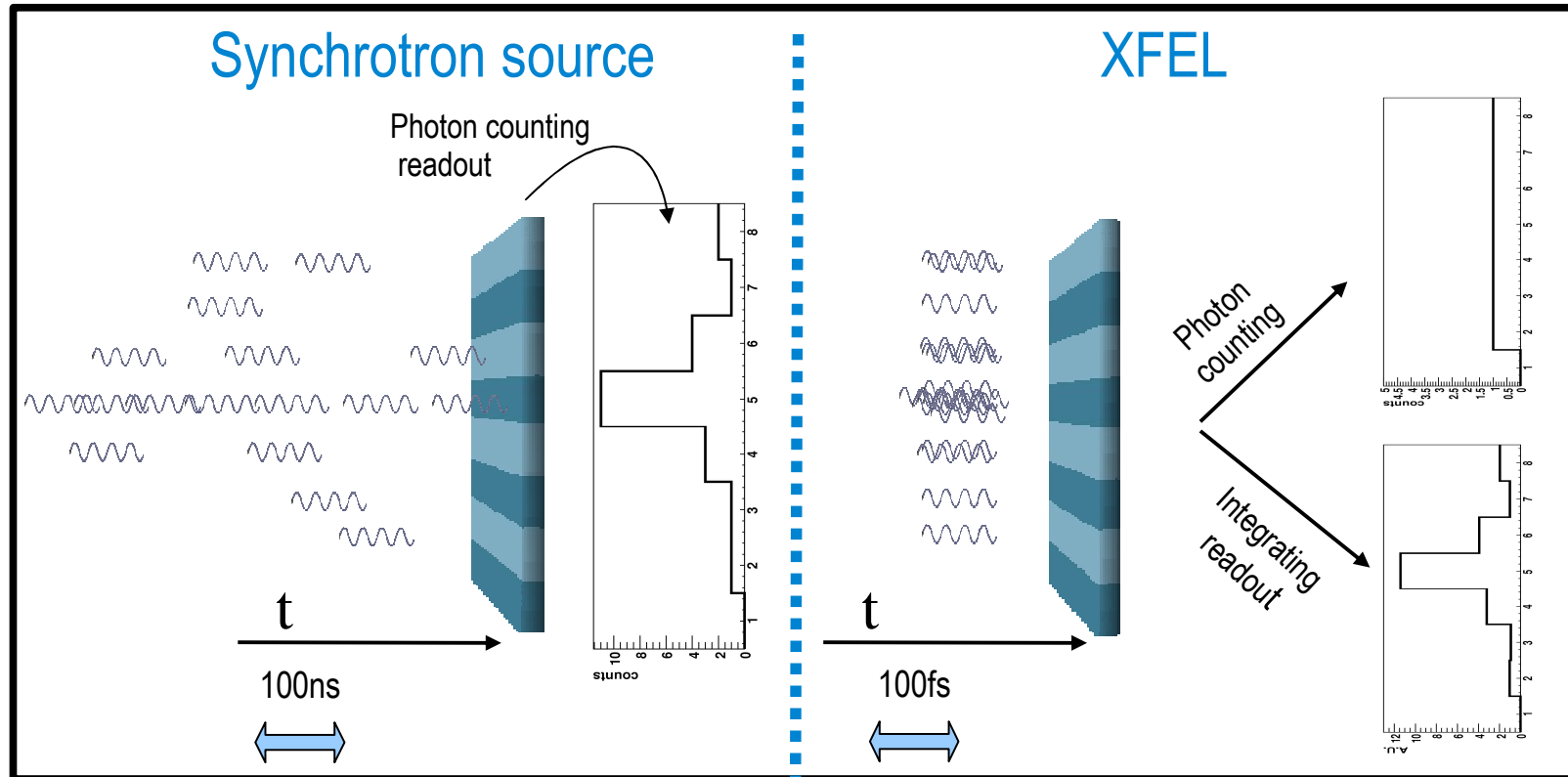


- UMC250; Rad hard design

- 256×256 pixels – 65536 pixels
- Pixel size $75 \times 75 \mu\text{m}^2$
- Count rate: $\sim 1 \text{ MHz/pixel}$
- Detector frame rate: $\sim 24 \text{ kHz}$



XFELs and synchrotrons



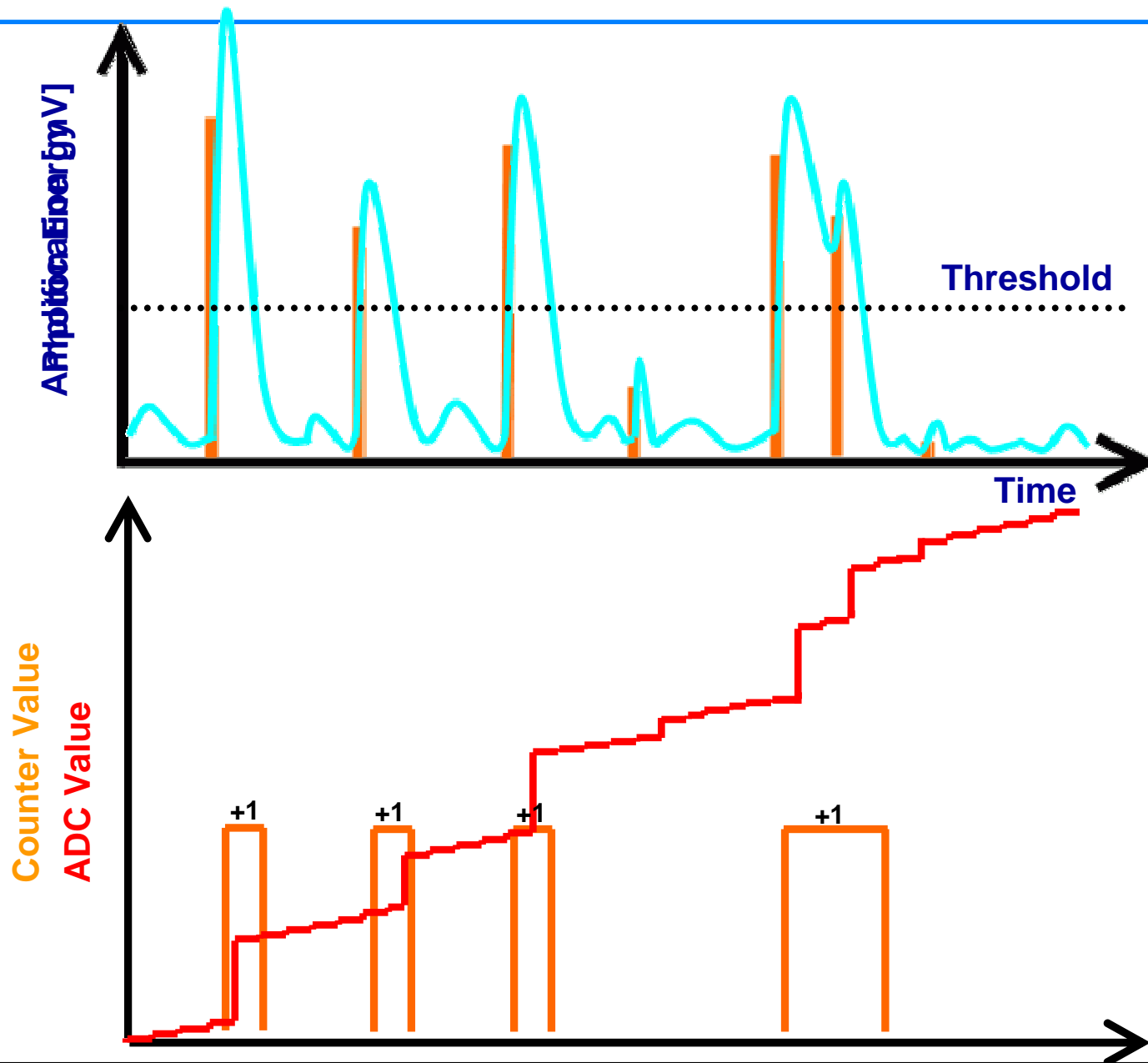
Synchrotron source:

- Huge number of “weak” photon bunches
- Photons impinge on the detector with a random time distribution
- Several tens of kGy

XFEL:

- Fewer intense bunches
- All photons coming at once
- High number of particles/channel per bunch
- EUXFEL: 1GGy

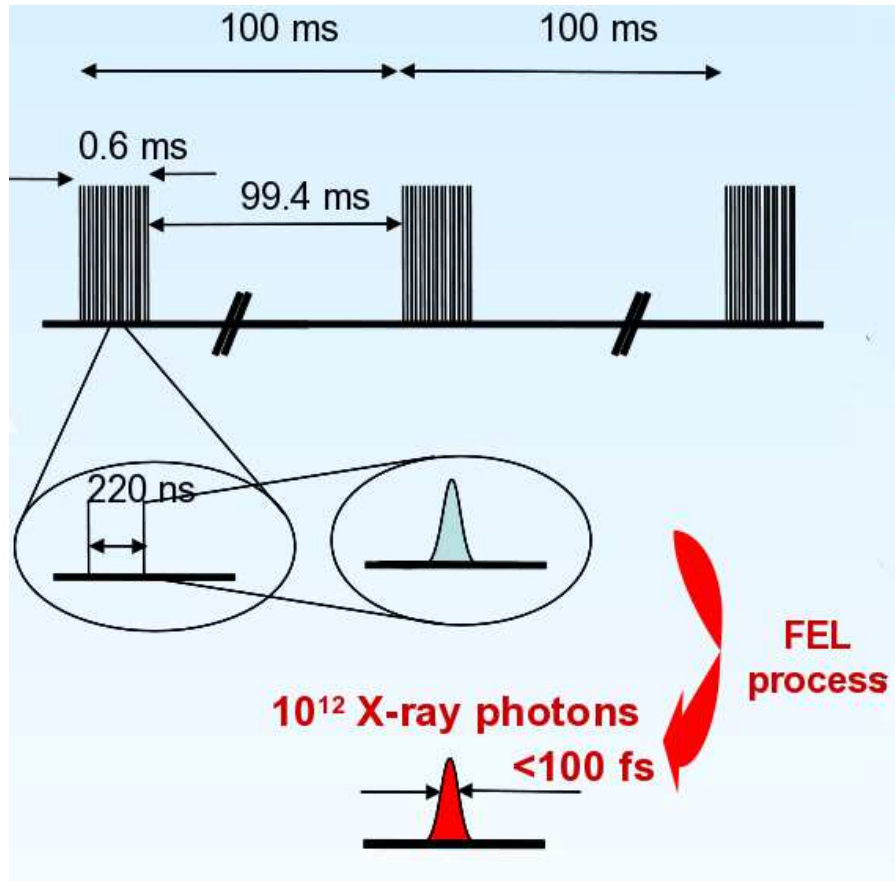
Counting vs. Integrating



Common chip testboard



XFEL detector development



Beam Characteristics:

- Bunch train repetition rate 10 Hz
- Bunch train duration 600 μs
- Number of bunches/train 2700
- Separation of bunches 220 ns
- (SASE) Each bunch consists of $\sim 10^{12}$ ph arriving in $<100\text{ fs}$
- Wavelength: 0.1-6 nm (12.4-0.2 keV)

Challenges:

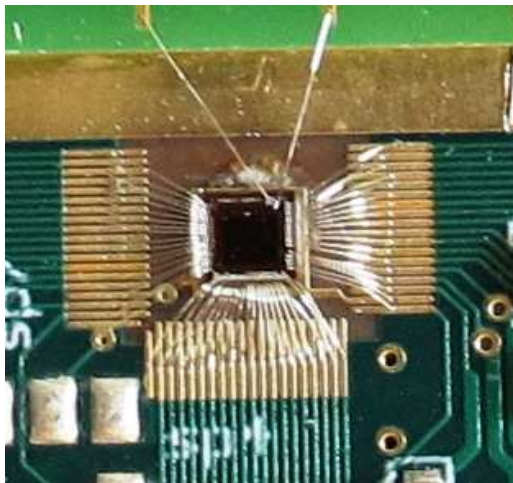
- „Instantaneous“ energy deposition
- Large number of bunches per train
- Very short separation between bunches
- Large dynamic range:
0 to 10^4 ph/pixel (= $3.3 \cdot 10^6$ e/pix)
- Single photon resolution (low rate regions)
- Limited by statistic (high rate regions)
- High radiation tolerance: expected to survive a \sim GRad dose (on the sensor)

AGIPD 1

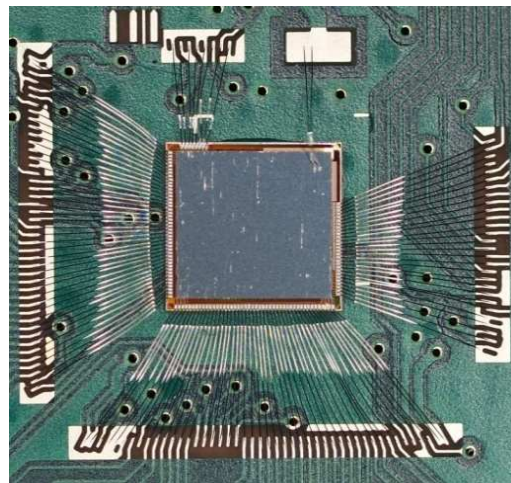
Active area: $13 \times 13 \text{ mm}^2$

- 64x64 pixels
- $200 \times 200 \mu\text{m}^2$ pixel
- Single photon resolution
- Dynamic range $10^4 \times 12 \text{ keV ph.}$

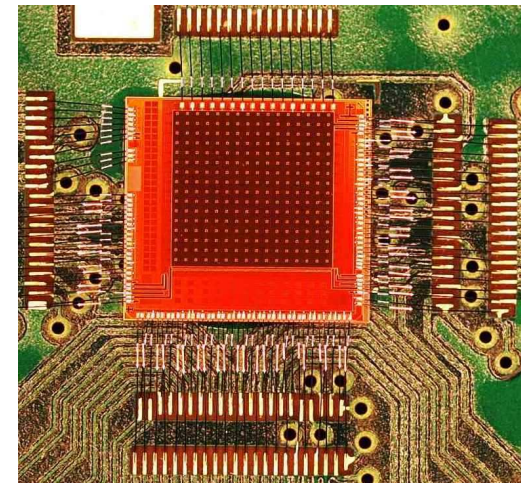
AGIPD 0.1



AGIPD 0.2

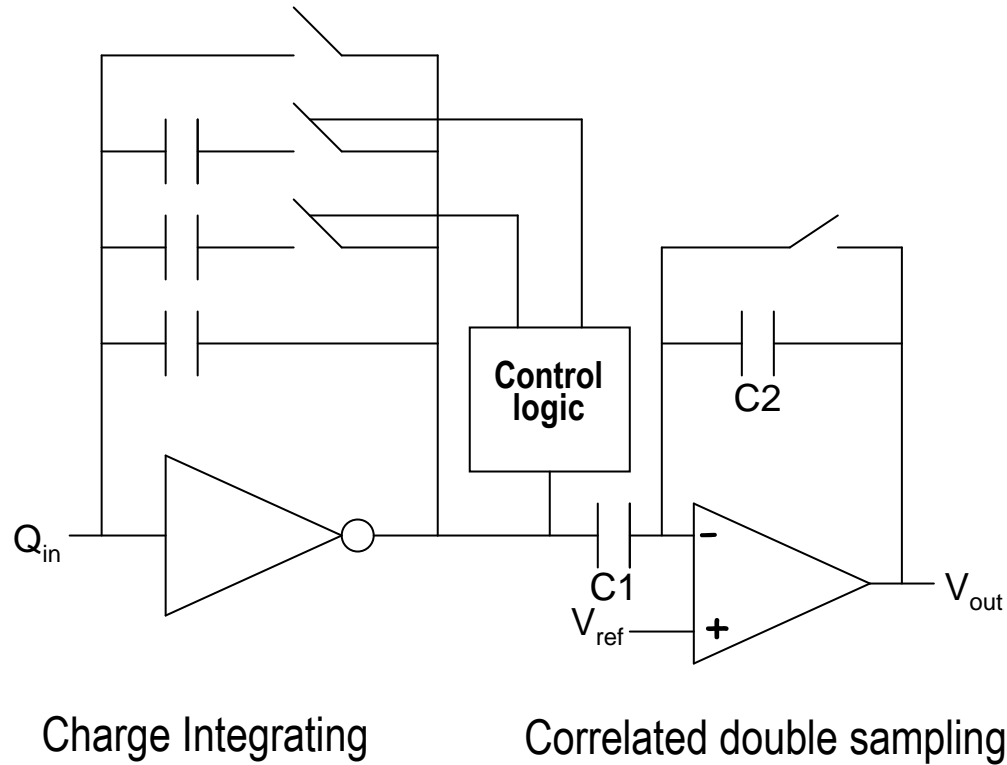


AGIPD 0.3

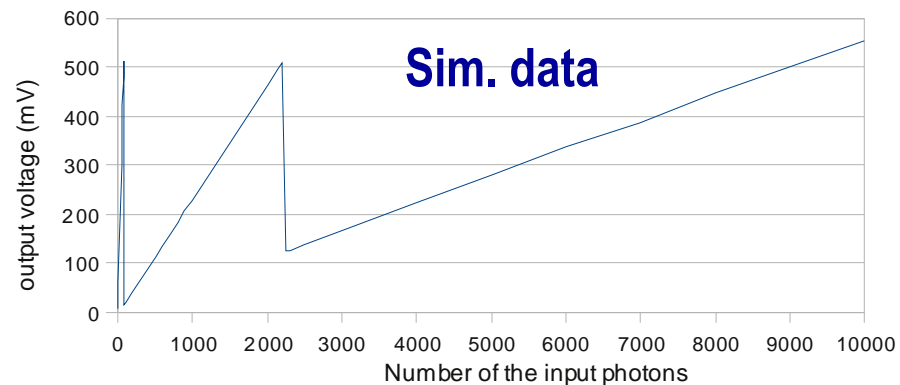


(DESY, Bonn, Hamburg, PSI)

Preamplifier with automatic gain switching

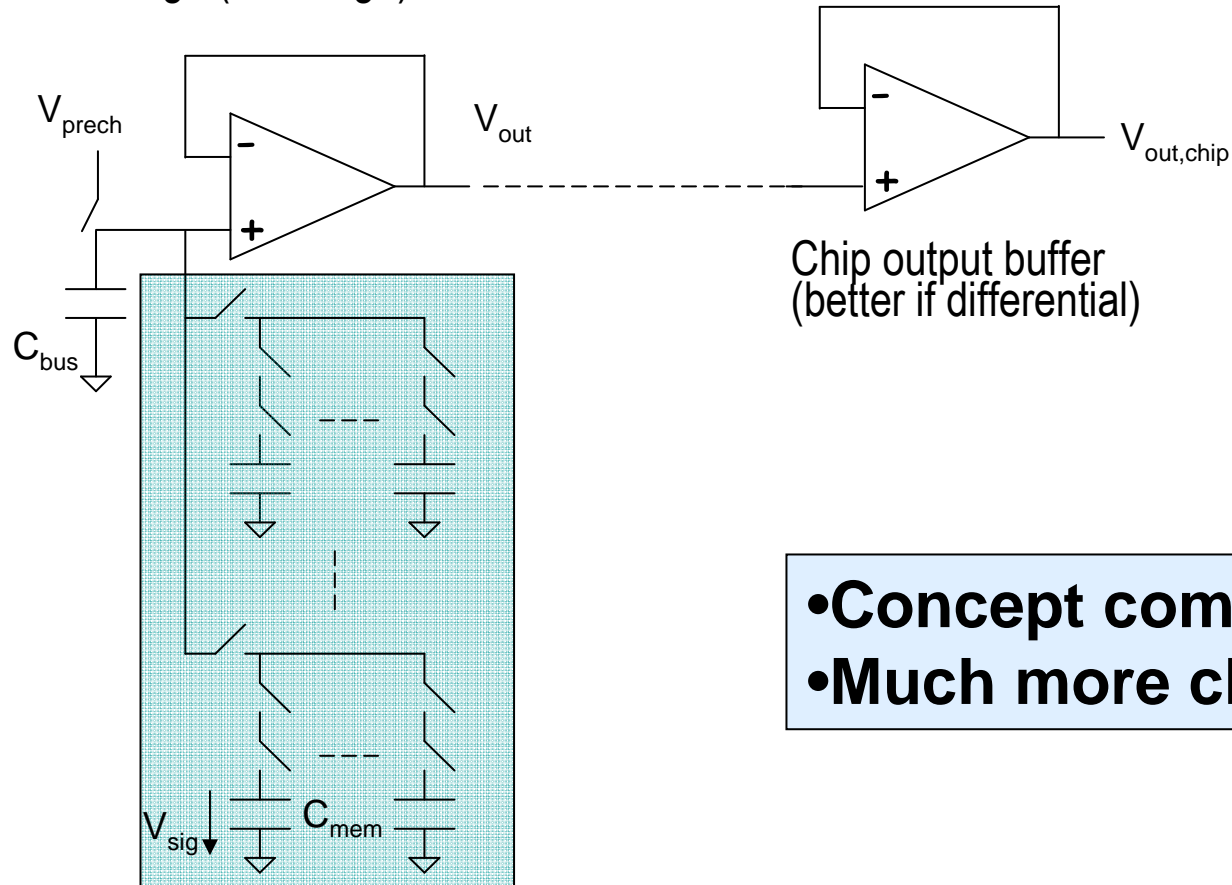


- Common for 1D and 2D
- The gain is automatically adapted to the input photons.
- Single photon resolution + high dynamic range.
- Correlated double sampling buffer reduces reset noise and 1/f noise.



Storage and readout

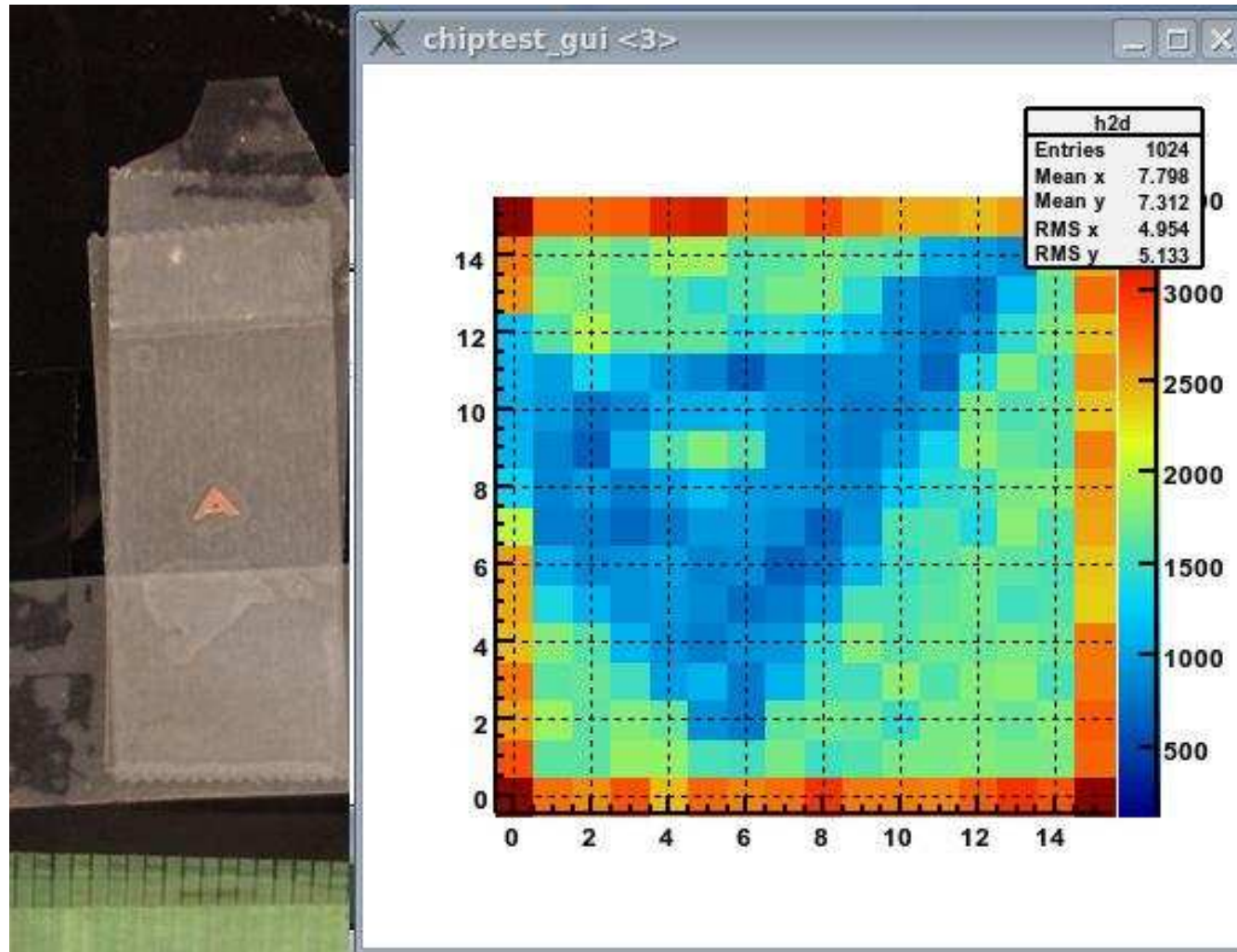
Voltage (or charge) local readout buffer

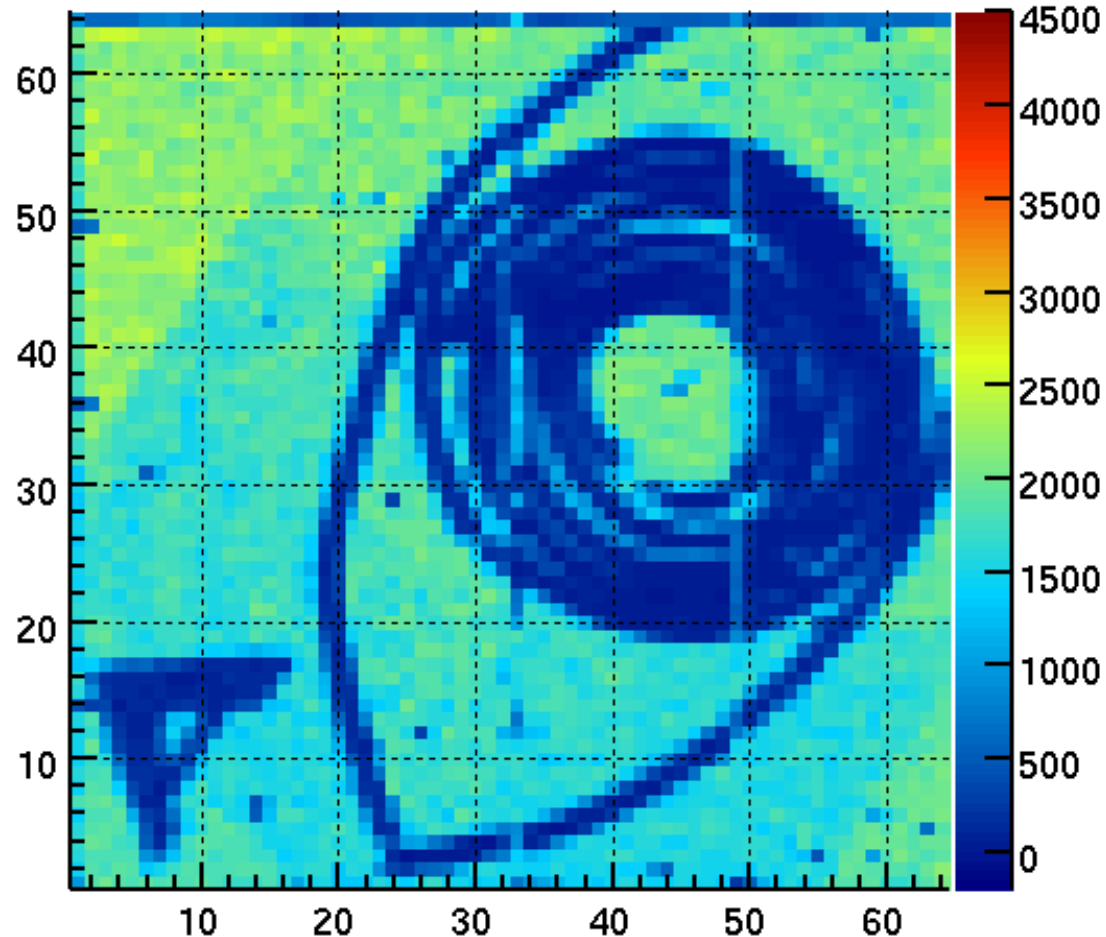


Chip output buffer
(better if differential)

- Concept common for 1D and 2D
- Much more challenging for 2D

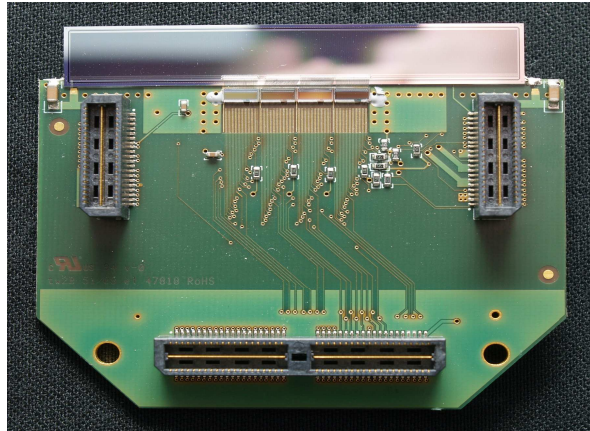
AGIPD02 – First Image



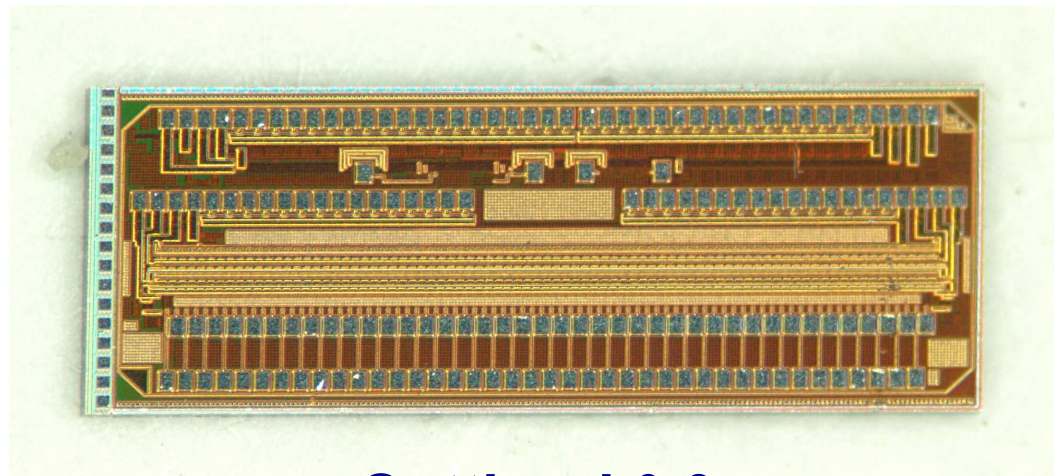
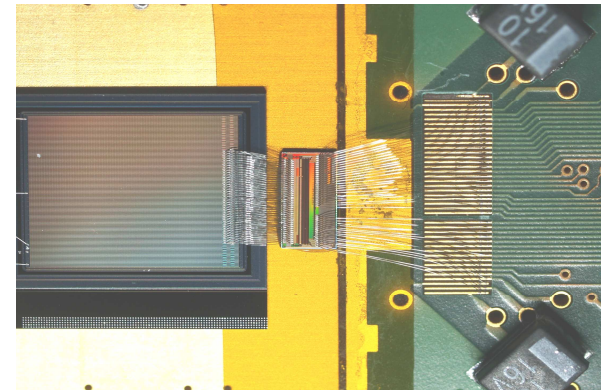


- Noise $\sim 300e^-$
- Dynamic range $\sim 10^4$ photons @ 12keV
- Frame rate not yet there, ok for full detector prototyping

Gotthard 0.1

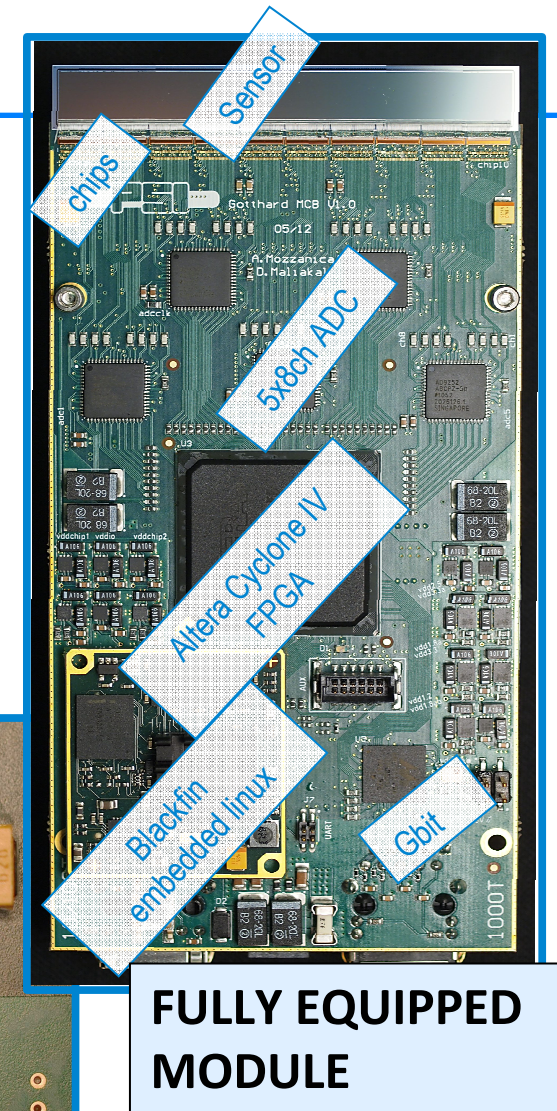
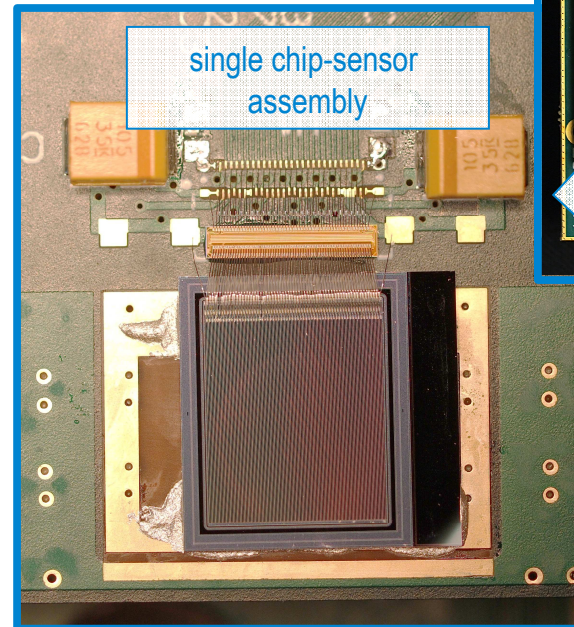


Gotthard 0.2



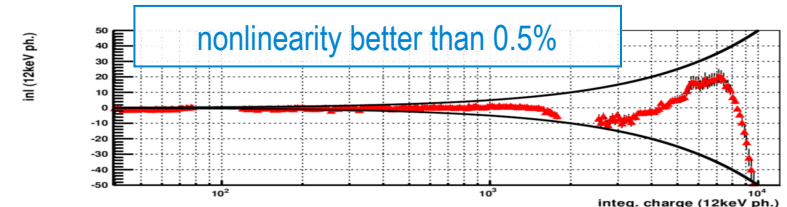
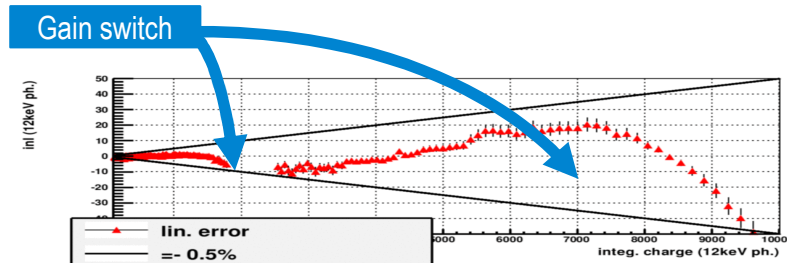
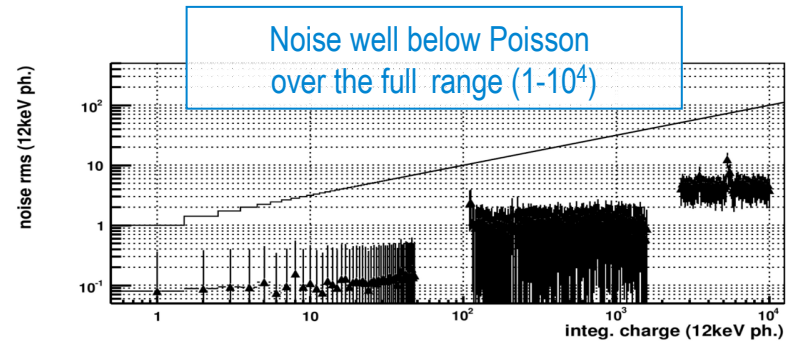
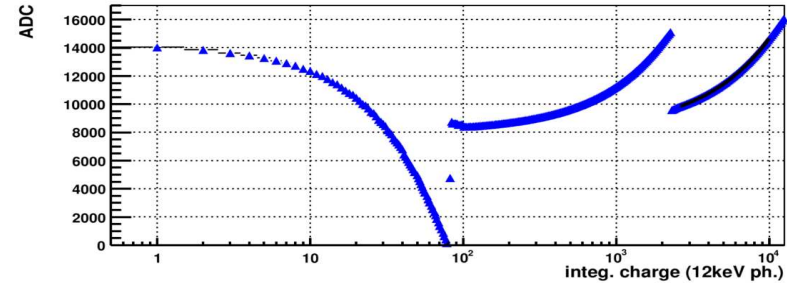
Gotthard 0.3

- 6.3x1.4mm² - 128 channels - 50μm pitch
- 3 automatic gain stages + 1 High Gain mode
- fast off pixel buffers, to sustain 32MHz readout with no cross-talk
- 4 diff. analog outputs, 8 digital (gain) outputs
- ~ 1mW/ch.
- Produced in a MPW run (130nm IBM), shared engineering run foreseen

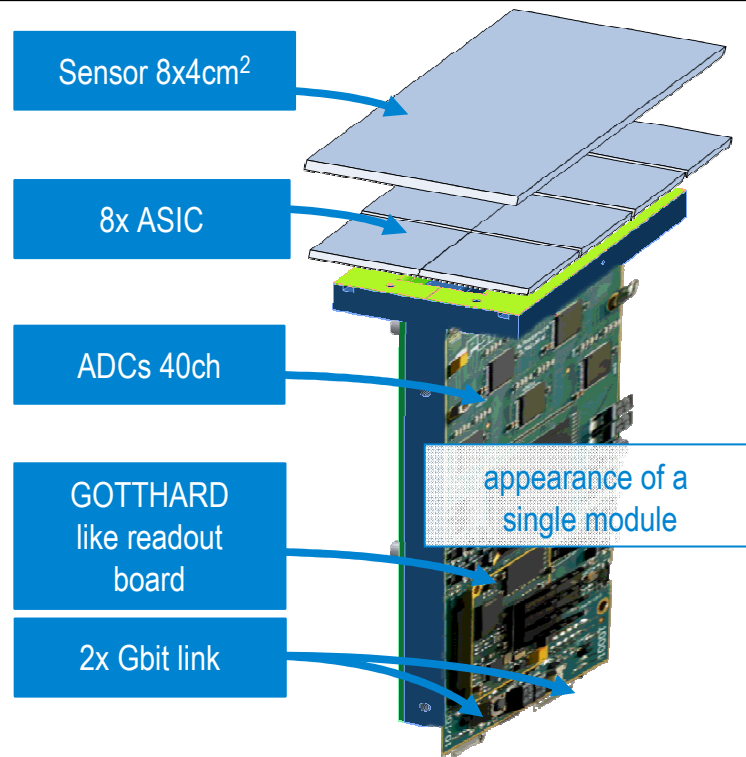


	Specifications
module size	6.7x13 cm
sensitive area	64x10mm
sensor thickness	320-500 μm
pitch	50 μm
dynamic range	10^4 12keV photons
min Energy	<3.5 keV
linearity	better than 0.5%
point spread function	O(pitch)
min int. time	80ns
dead time	<50ns
cooling	air (fan)
readout time = 1 / frame rate	>50kHz continuous 1MHz burst
XFEL ready	YES

Noise (ENC), gain1: $260e^-$ rms

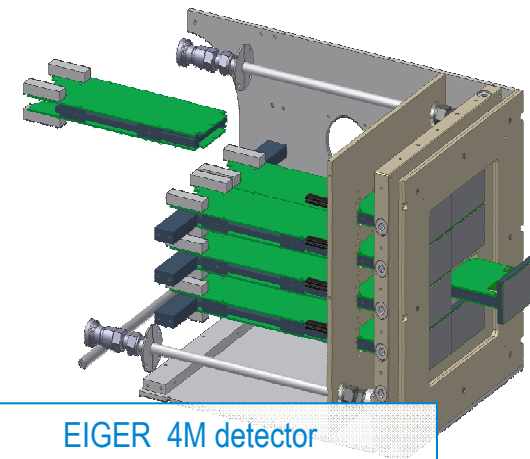


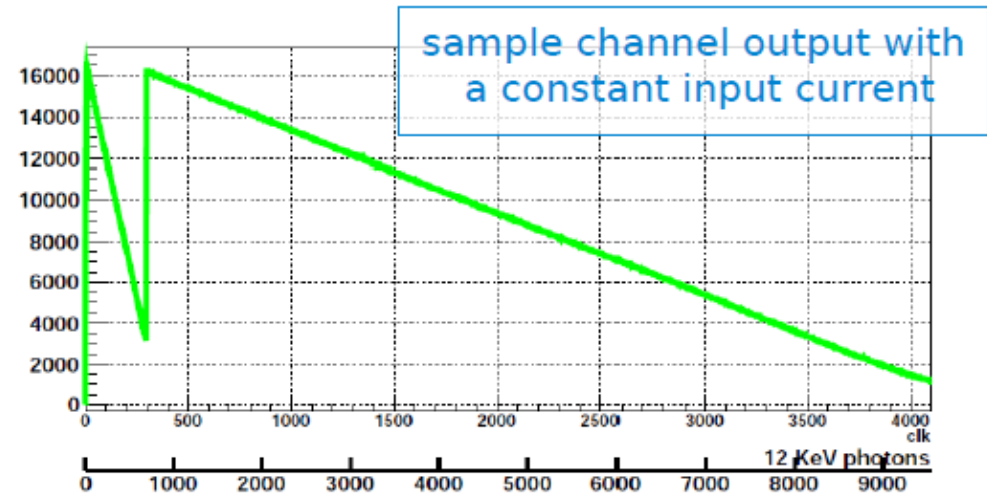
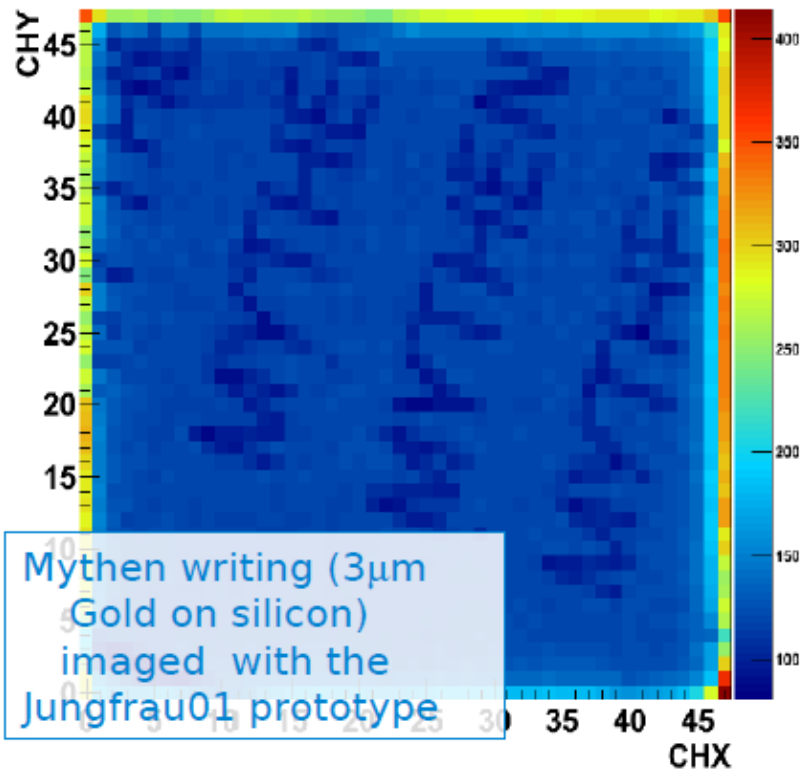
- **SwissFel** will have a much simpler bunch structure:
1 single bunch with a repetition rate of 100Hz, the detector can be read out in the interbunch time (no on-chip storage needed)



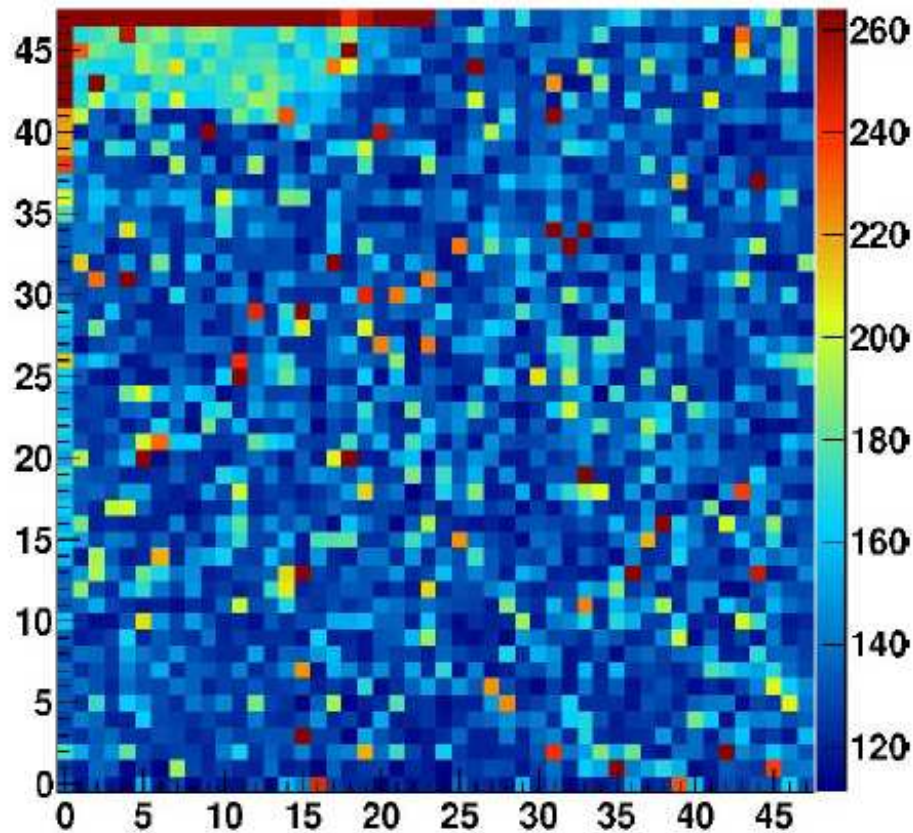
- ASIC and readout system based on GOTTHARD
- Dimensions, sensor and mechanics from EIGER

ASIC technology	UMC110nm
module pixel count	525k
module size	80x40 mm ²
sensor thickness	320-500 μm
pixel size	75x75 μm ²
dynamic range	up to 10 ⁴ 12keV photons
noise r.m.s.	<150 e.n.c.
min Energy	<3 keV
linearity	better than 1%
point spread function	1 pixel
dead time	<50ns
cooling	liquid
readout time = 1 / frame rate	400Hz

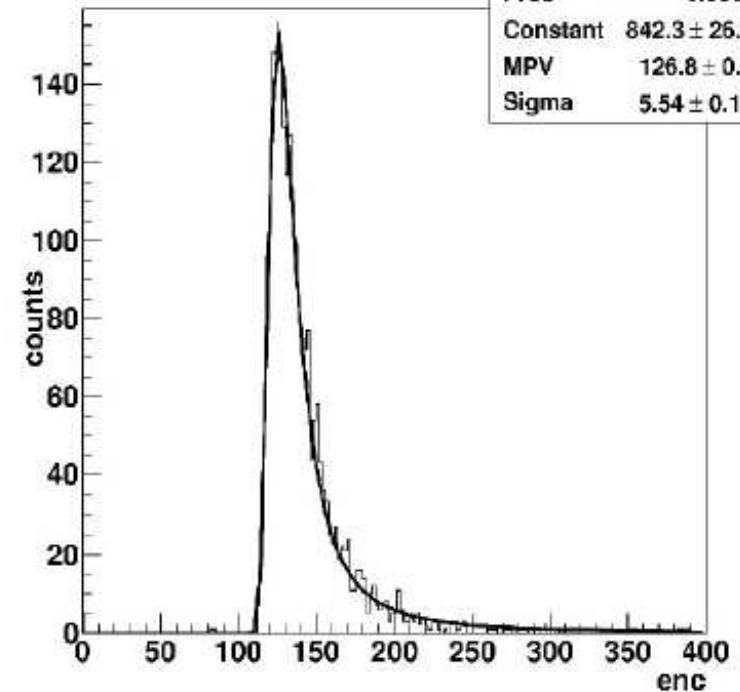




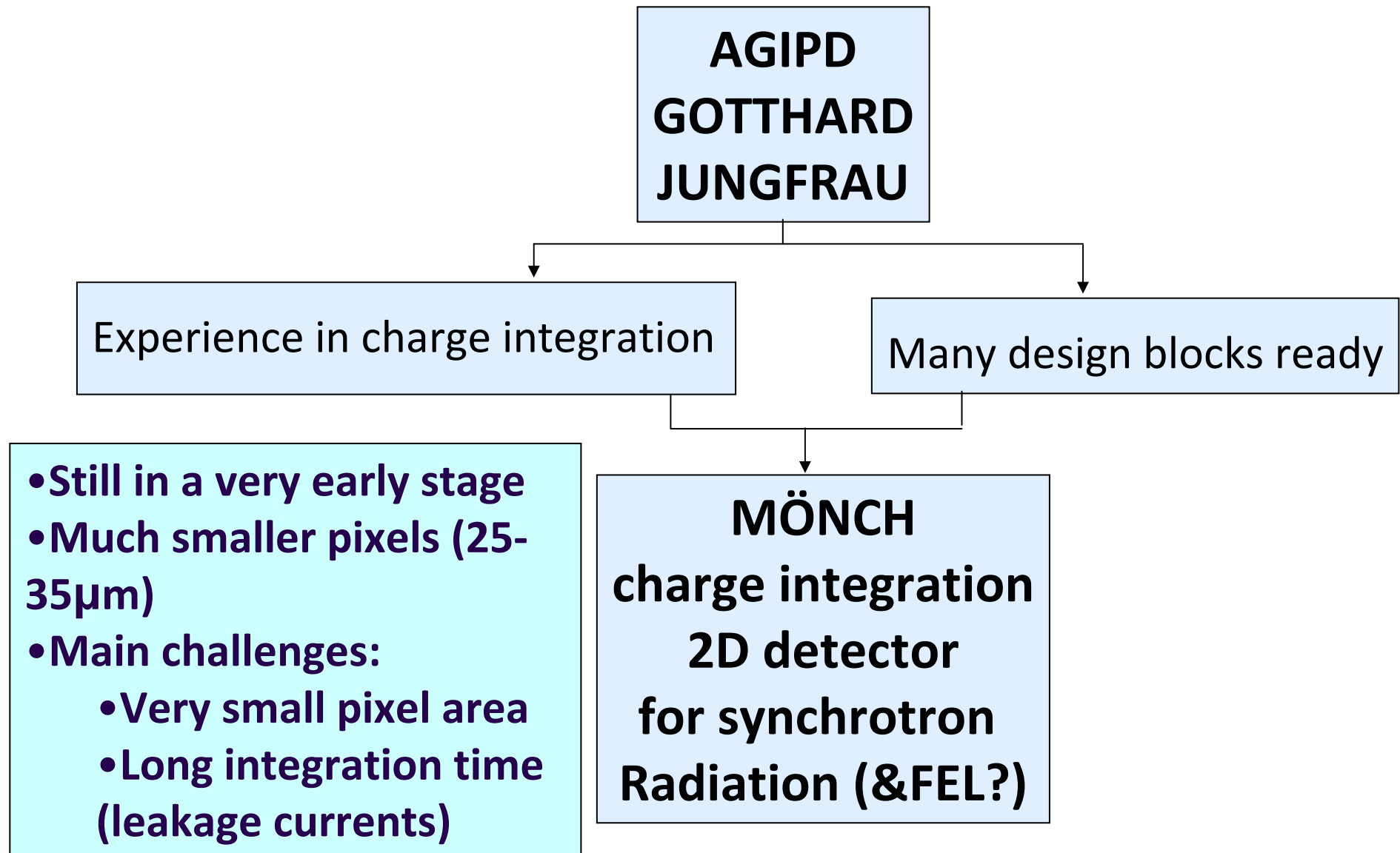
enc noise 2d



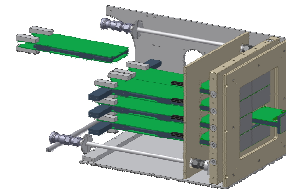
dist of enc



**Preliminary results with JUNGFRAU02:
Better but not yet solved.**



MÖNCH motivation



LOOKS WRONG... ☹️

MÖNCH motivation

3970 m

4107 m

4158 m



MUCH BETTER NOW 😊

Why charge integration at fine pixel pitch?

Pros:

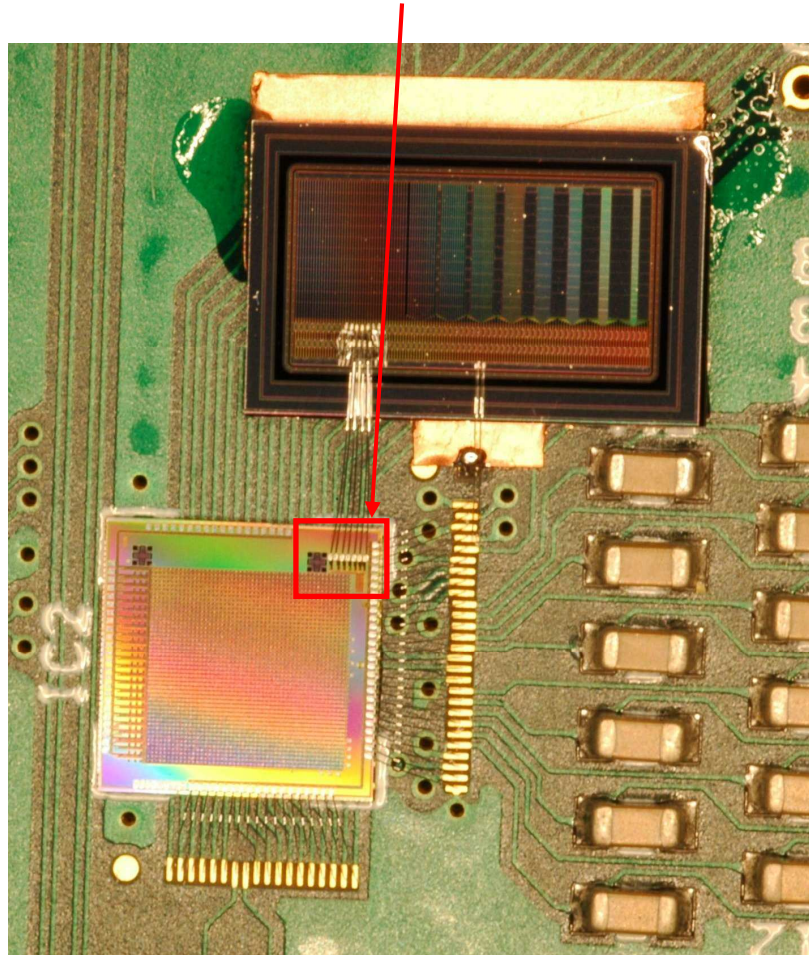
- Both XFELs and synchrotrons
 - Higher photon flux
 - Can still work with high charge sharing
 - + Charge sharing can improve resolution ("**low flux**" only)
- > *very small pixels are feasible!*
- Energy information ("**low flux**" only)
 - Lower energies (~keV) accessible

Cons:

- In "**low flux**": 1 photon/image per cluster of 4-9 pixels
- Leakage current challenge
- Bump-bonding swamp
- Calibration nightmare
- Data throughput/storage/analysis hell

MÖNCH prototypes

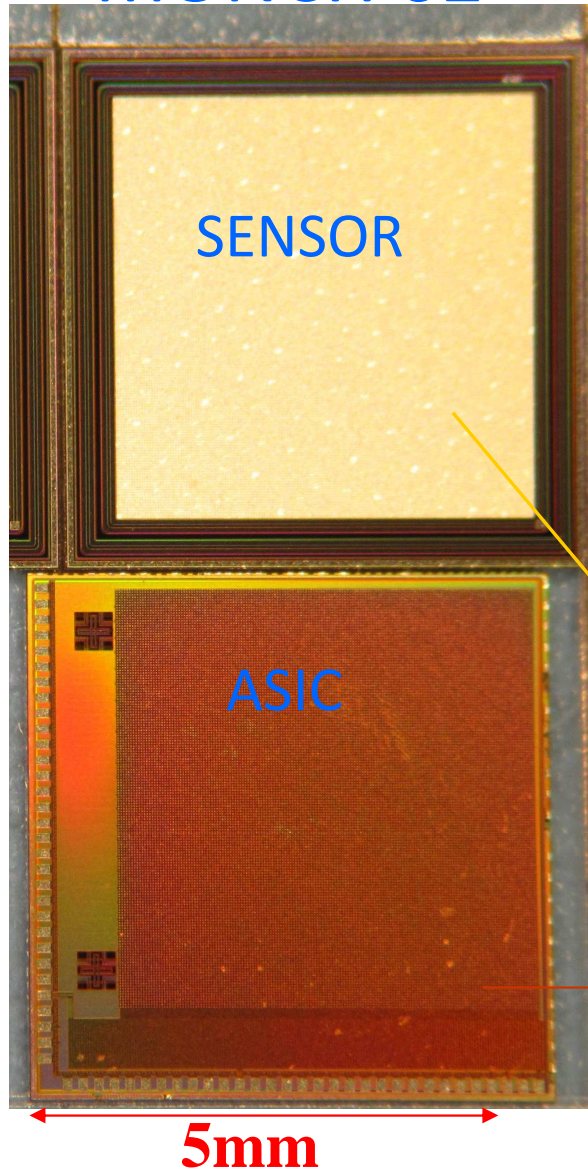
MÖNCH 01



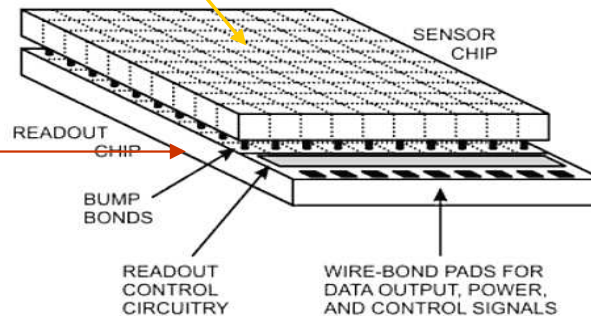
- UMC 110nm AE (AI only)

- “Guest” of Jungfrau01 MPW
- Seven single pixel channels
- No layout optimization
- No dynamic gain switching
- Wirebonded to a strip detector for calibration

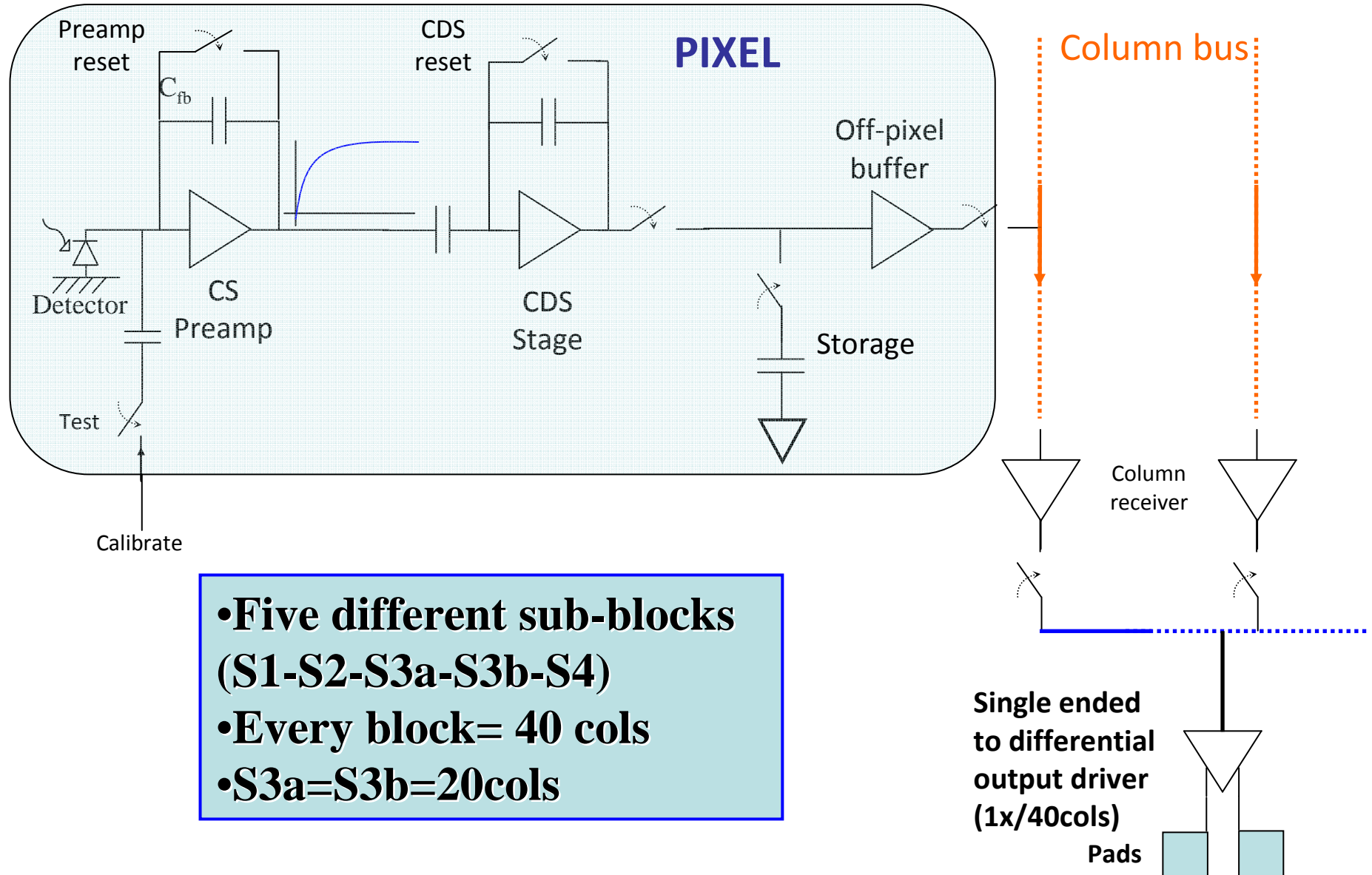
MÖNCH 02



- Complete hybrid pixel detector
- Active area: 4x4 mm²
- 160x160 pixels
- 25x25 μm² pixel
- Big layout optimization effort
- High testability

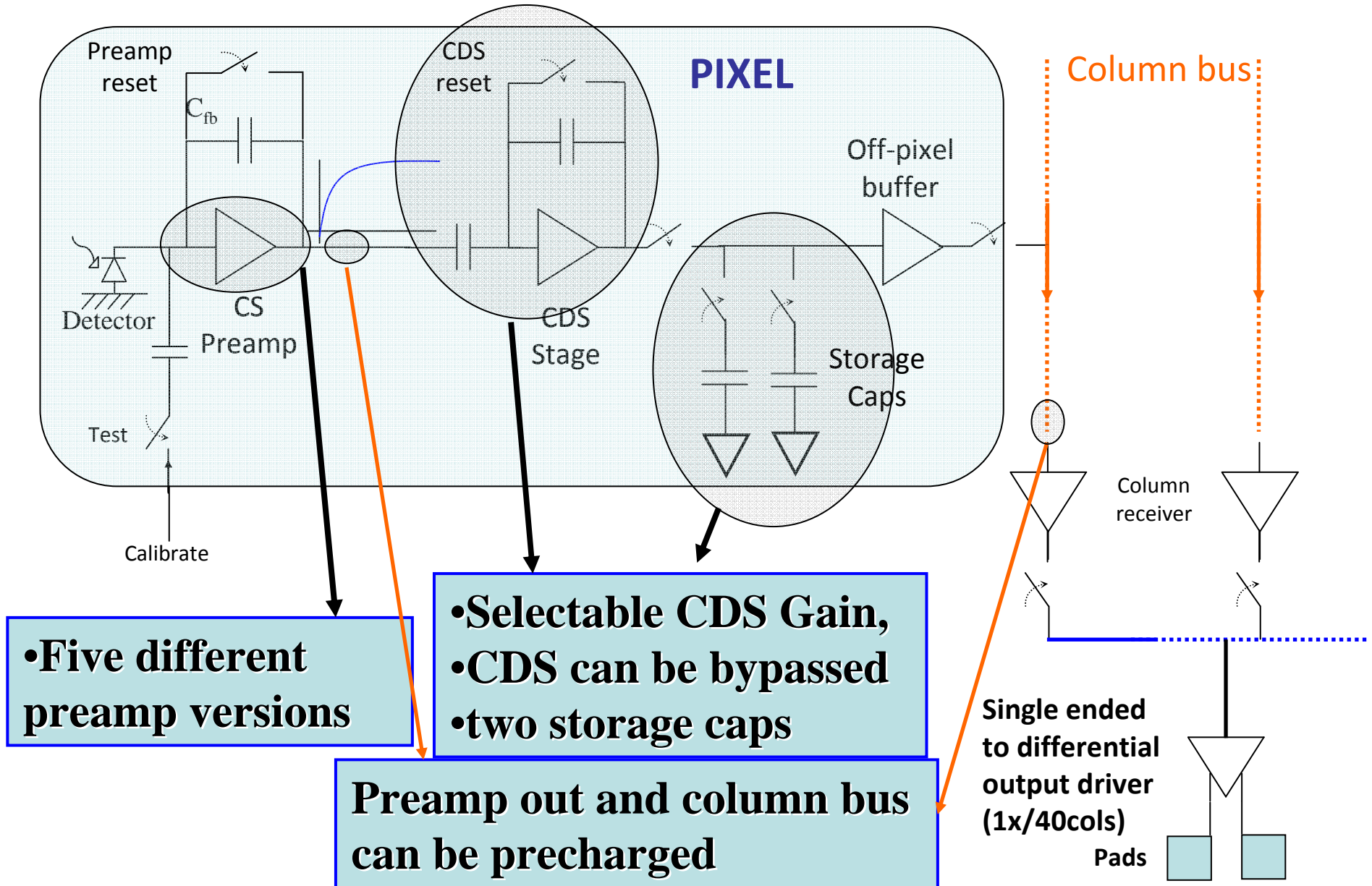


Basic working principle

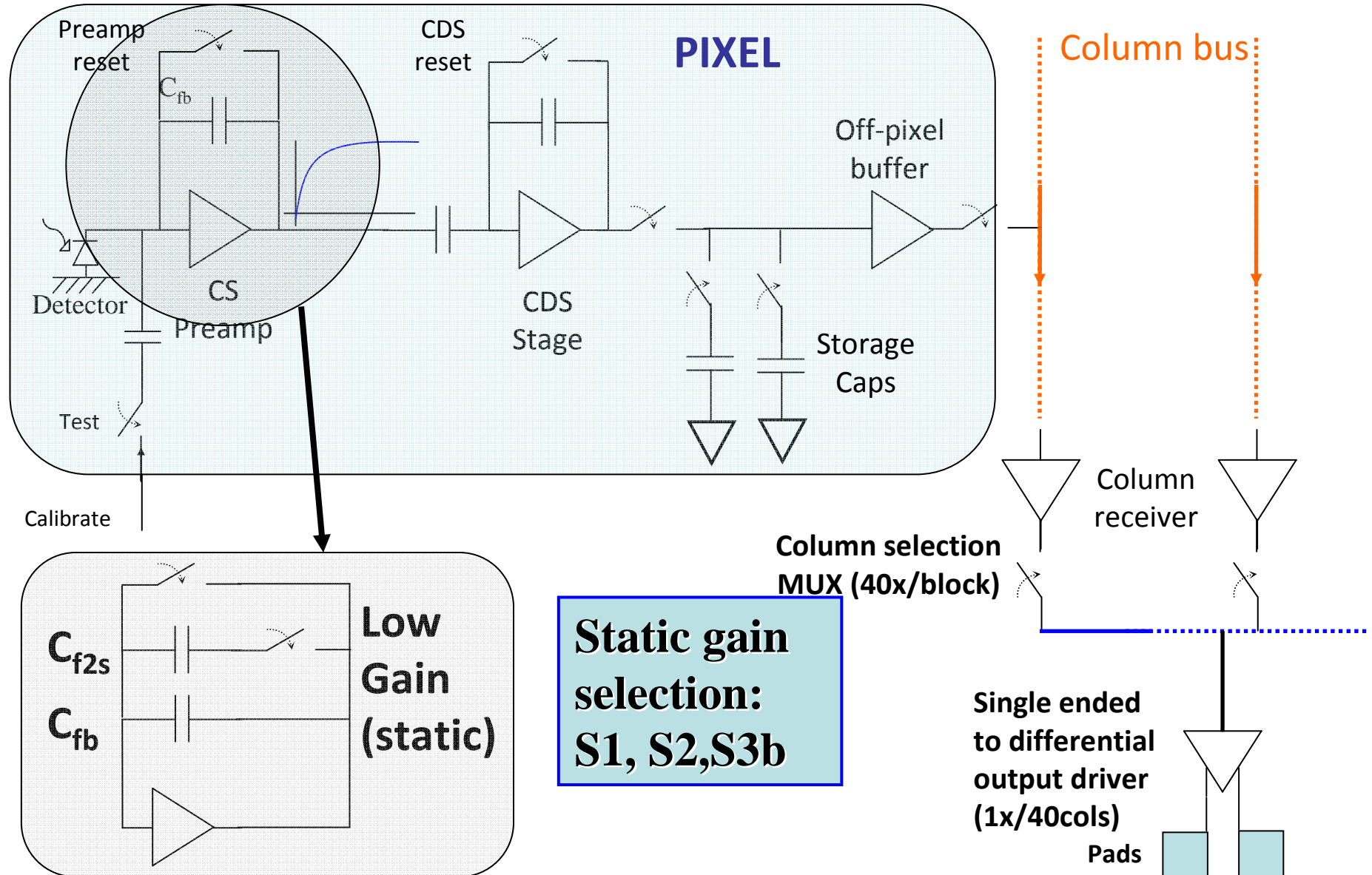


- Five different sub-blocks (S1-S2-S3a-S3b-S4)
- Every block= 40 cols
- S3a=S3b=20cols

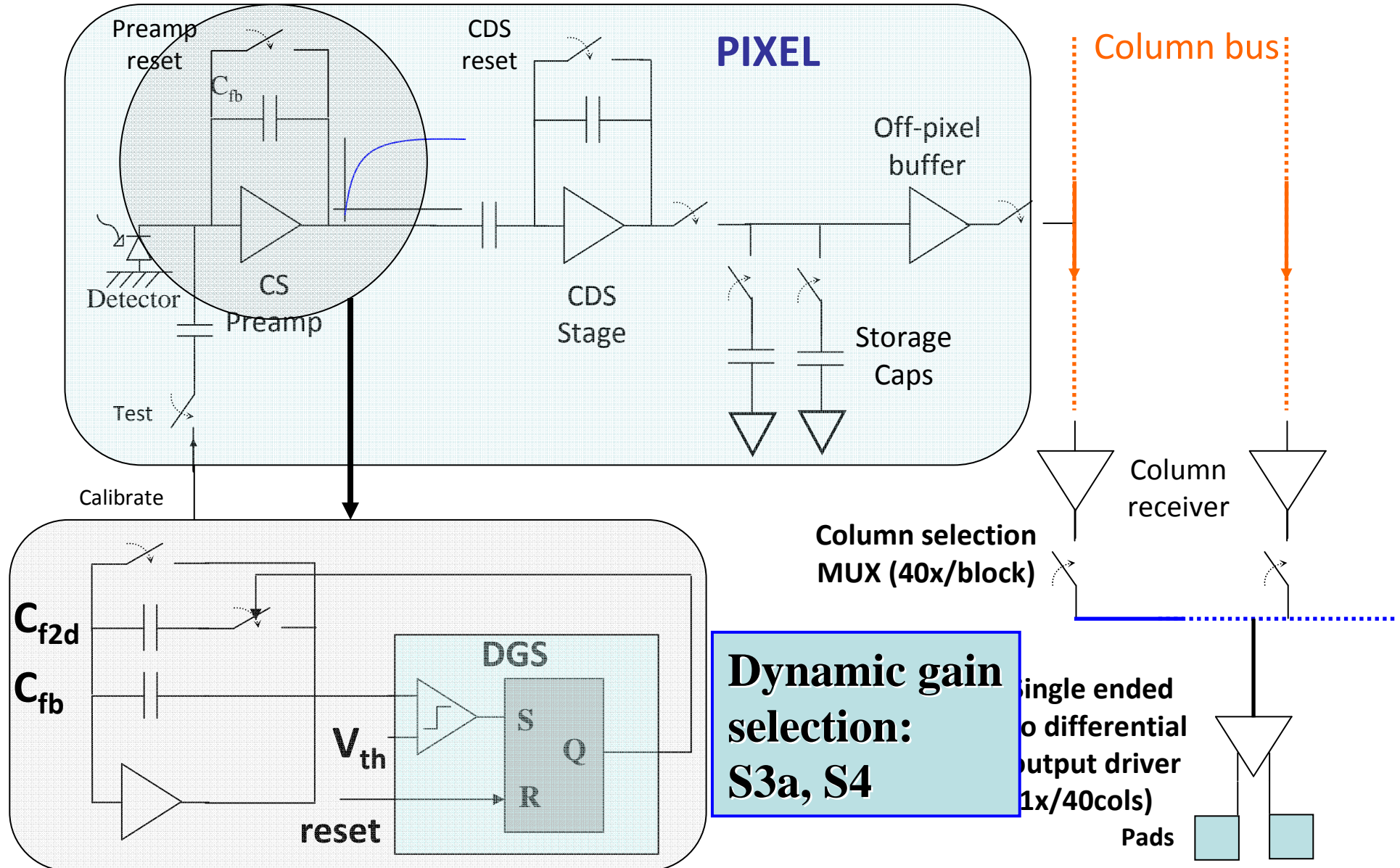
Variations



Variations

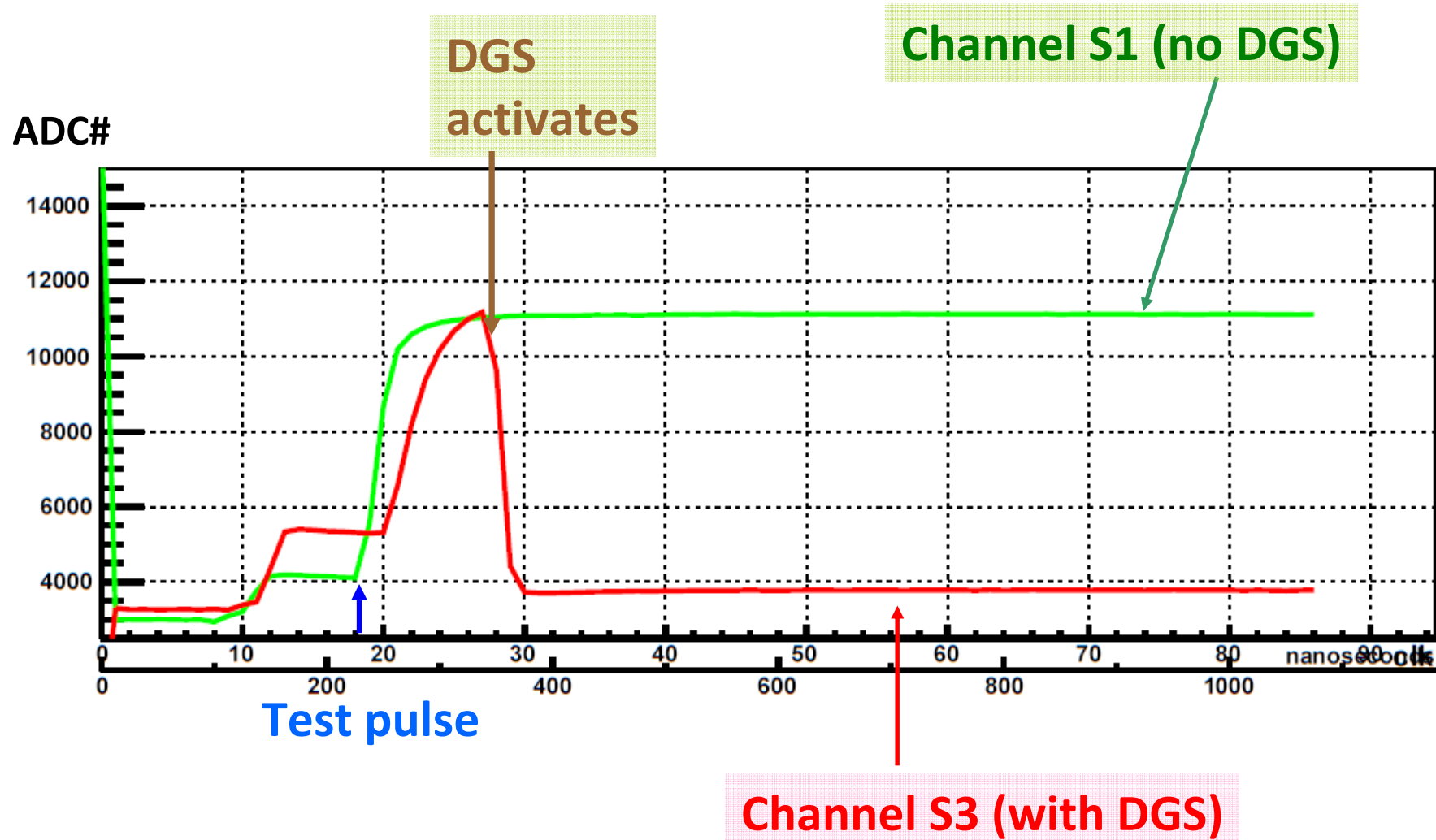


Variations

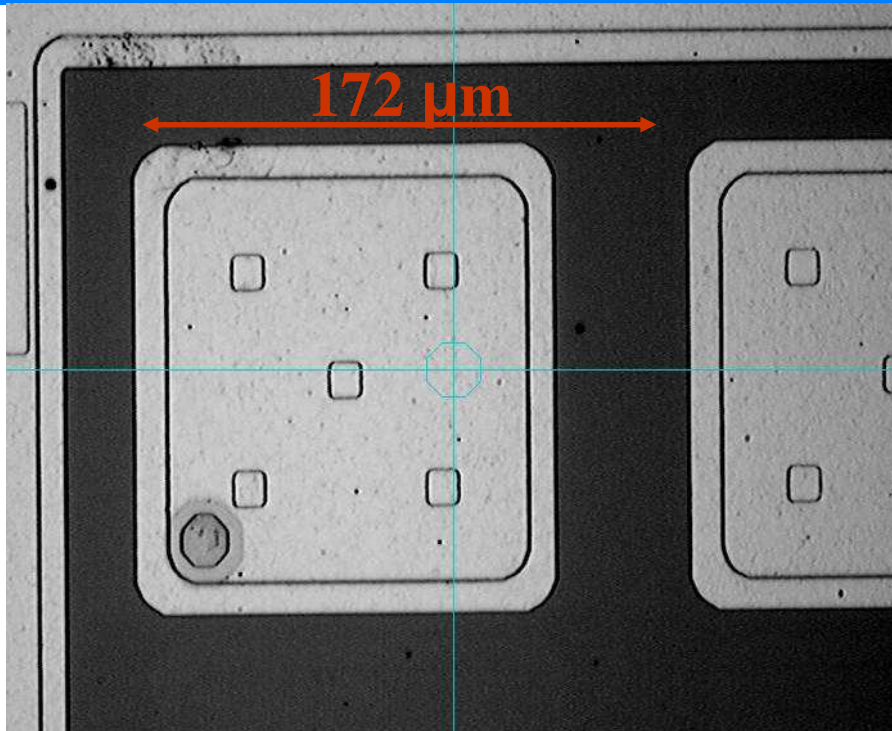


**Main test results:
PRELIMINARY!**

Dynamic Gain Switching functionality

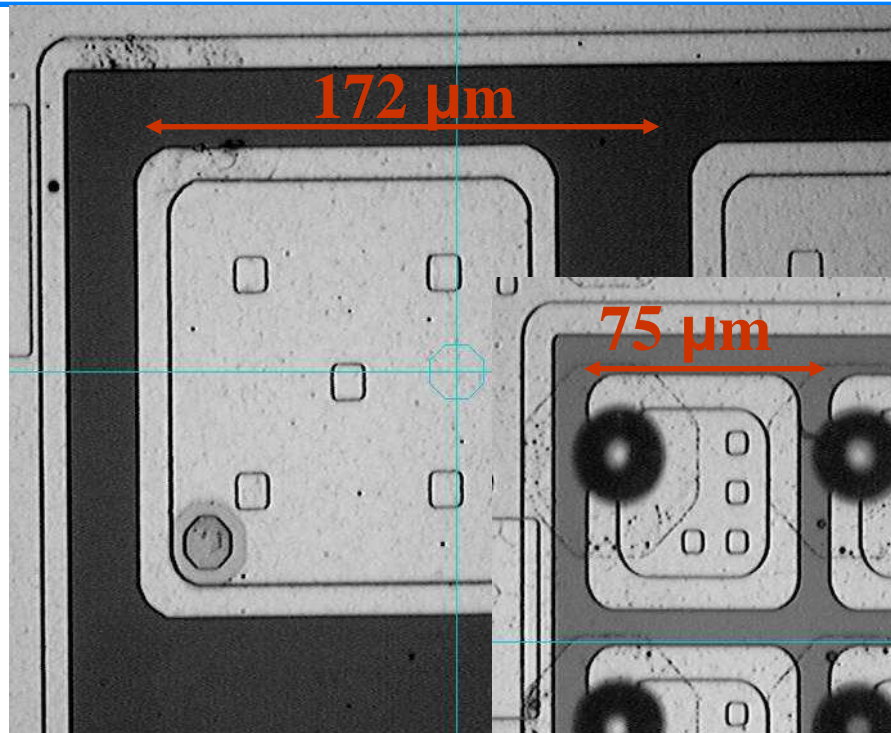


Fine pitch bump-bonding

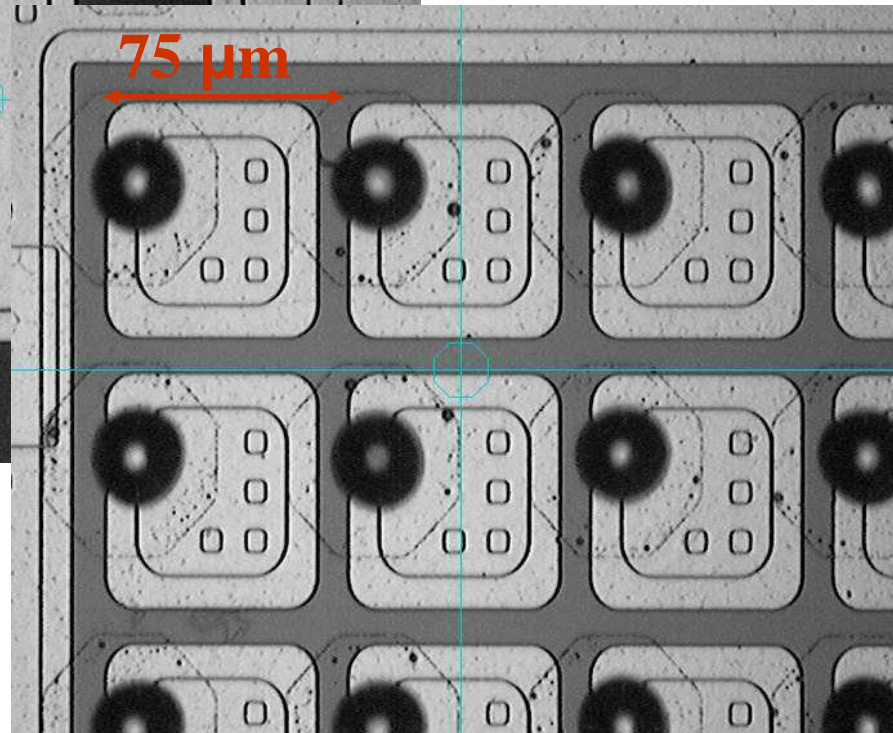


PILATUS

Fine pitch bump-bonding

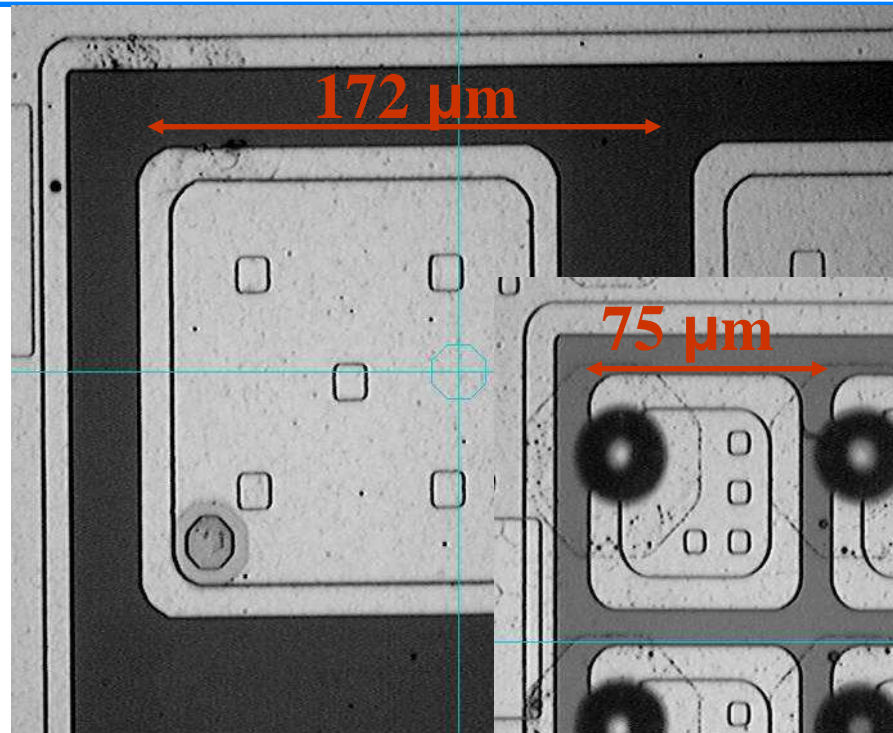


PILATUS

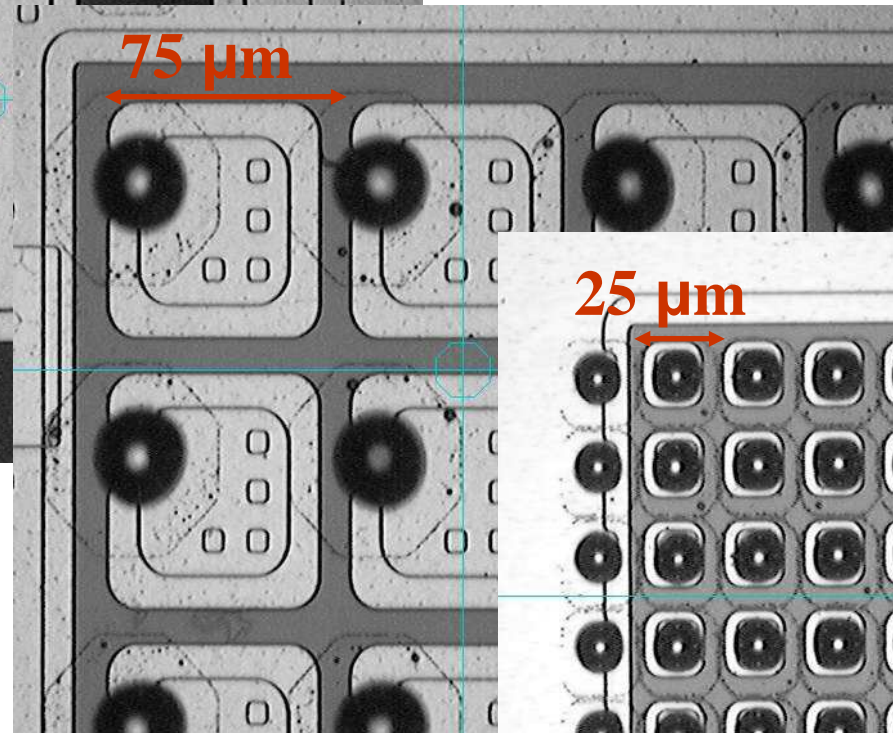


EIGER

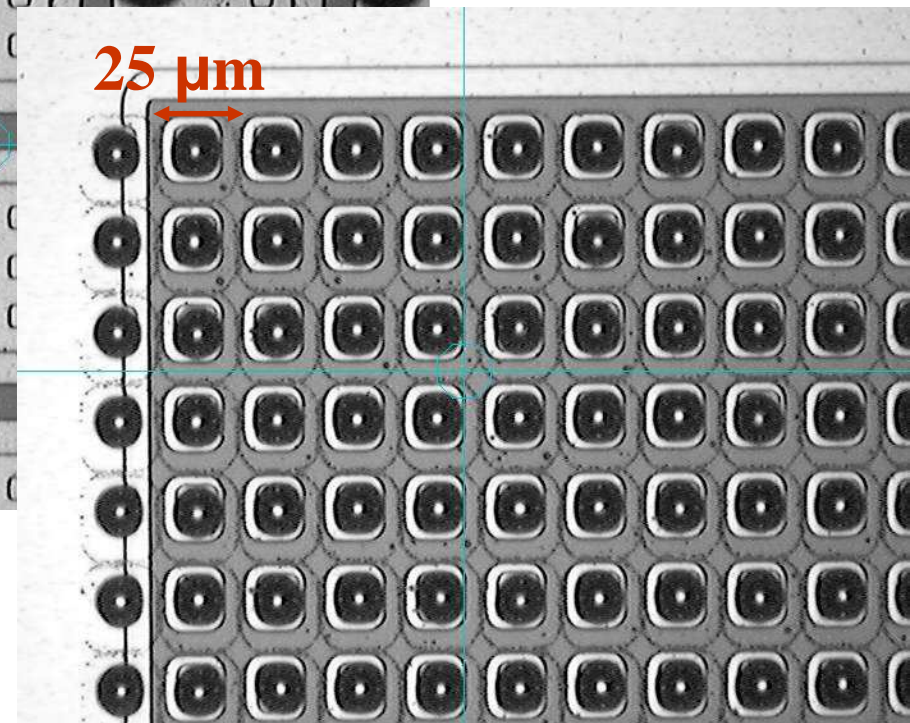
Fine pitch bump-bonding



PILATUS

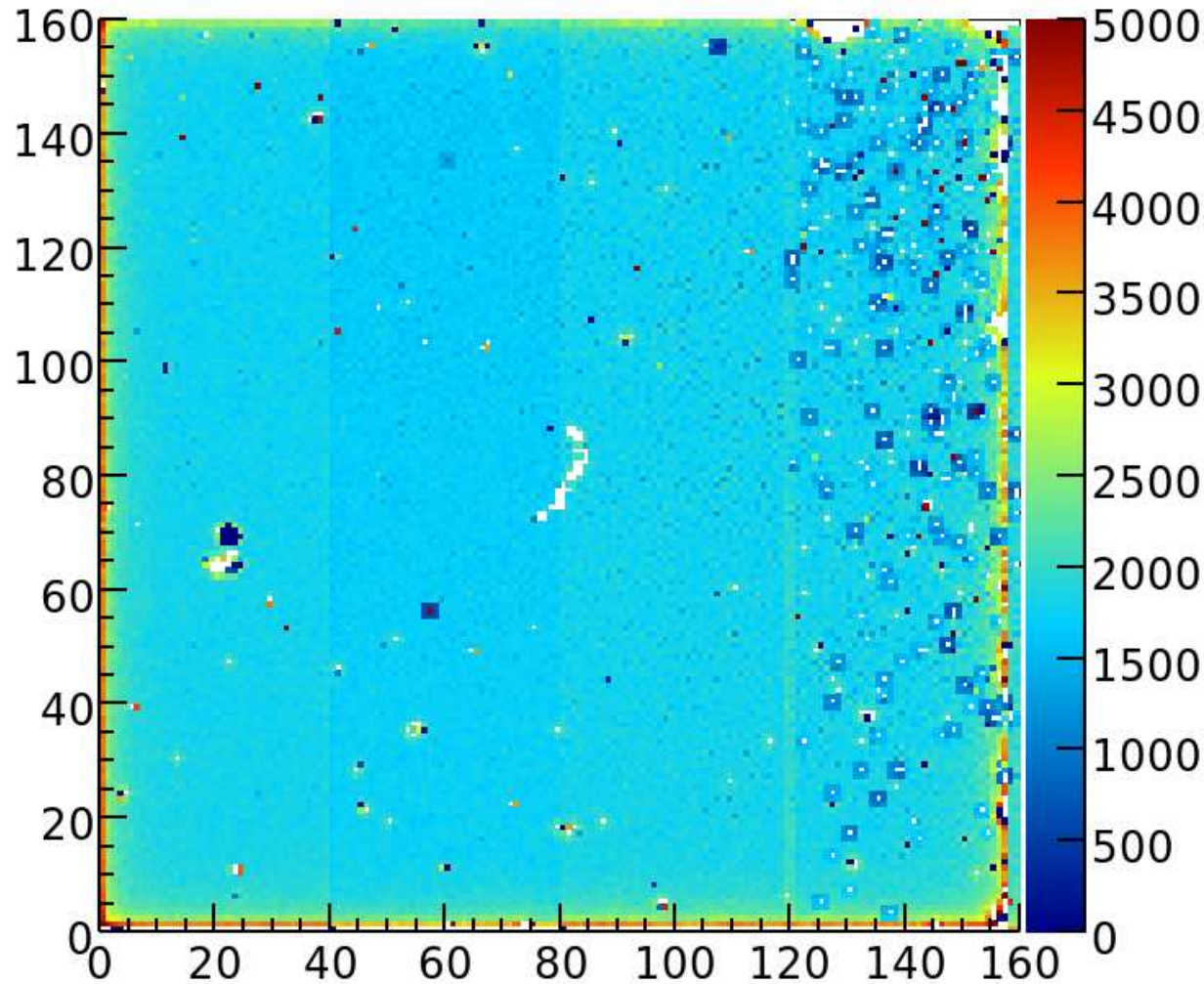


EIGER



MÖNCH

Bad pixels map



- Yield: ~97%
- Very first prototypes
- Single chip processing
- Low tolerance with UBM masks
- Alignment precision

**** Big improvement possible ****
(... and new bump-bonder coming..)

First images

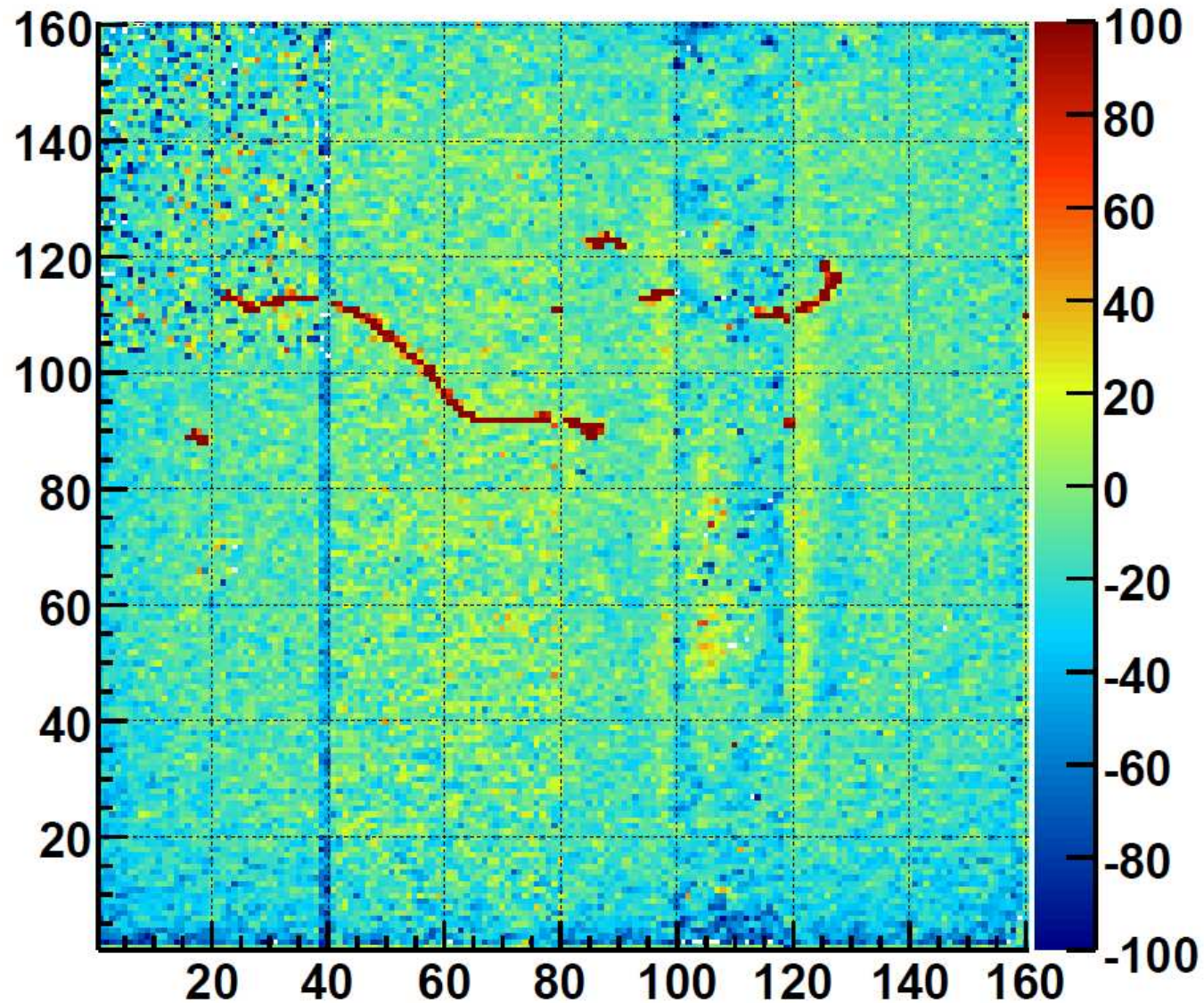
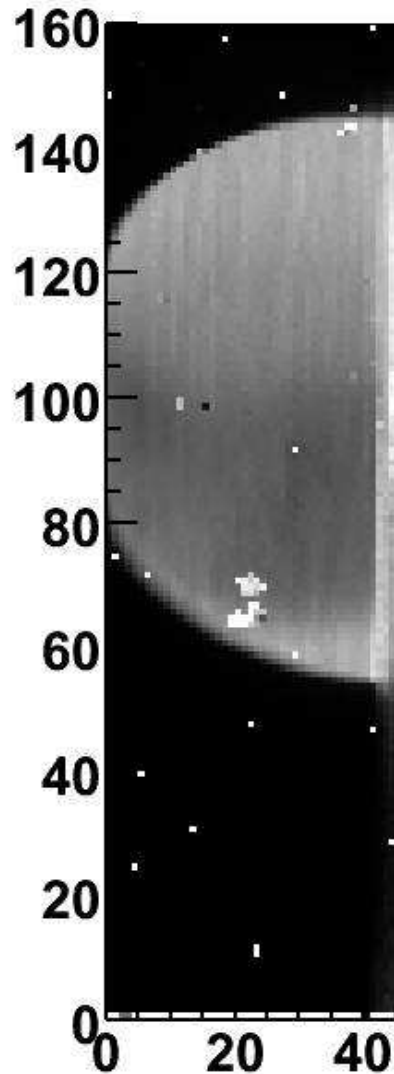


Image taken with
a ^{90}Sr radioactive
source.
**=>All blocks are
functional.**

Imaging tests (S1 only)

METAL RING

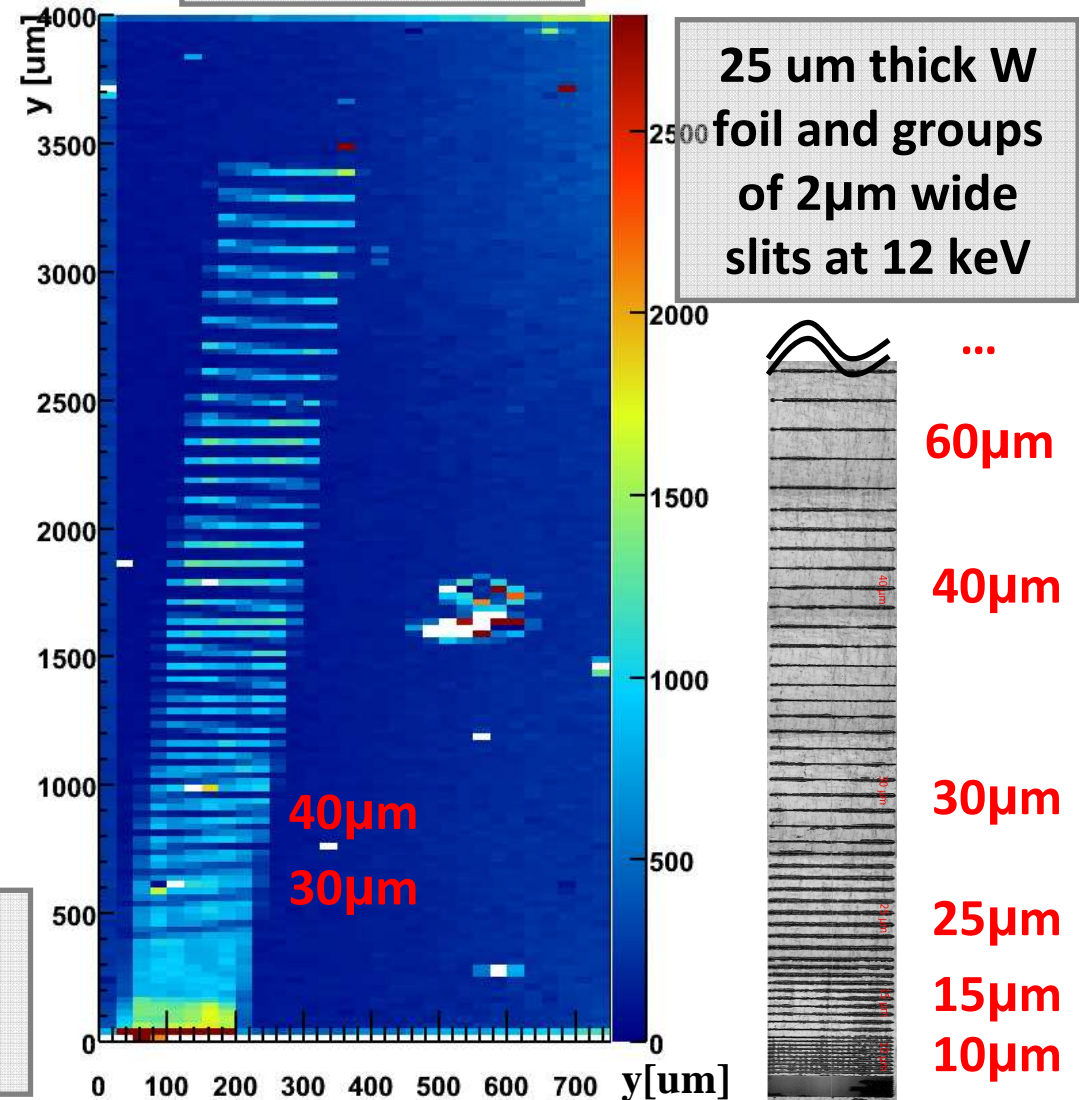


Fish egg(??)

Metal ring

Frame rate:
400Hz

W SLITS



25 um thick W foil and groups of 2um wide slits at 12 keV

60um

40um

30um

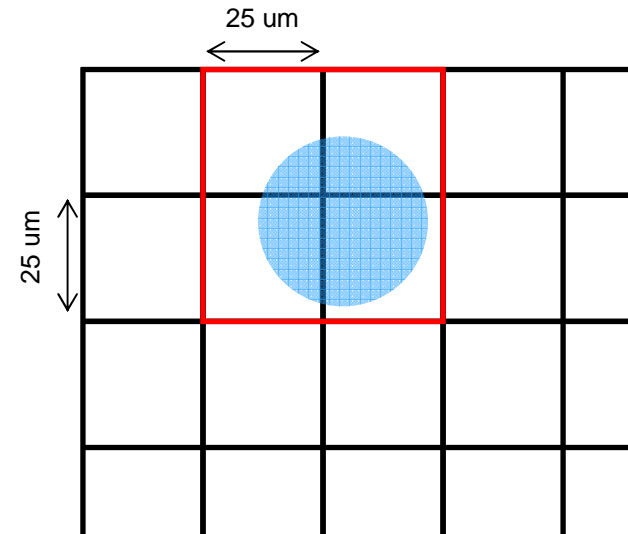
25um

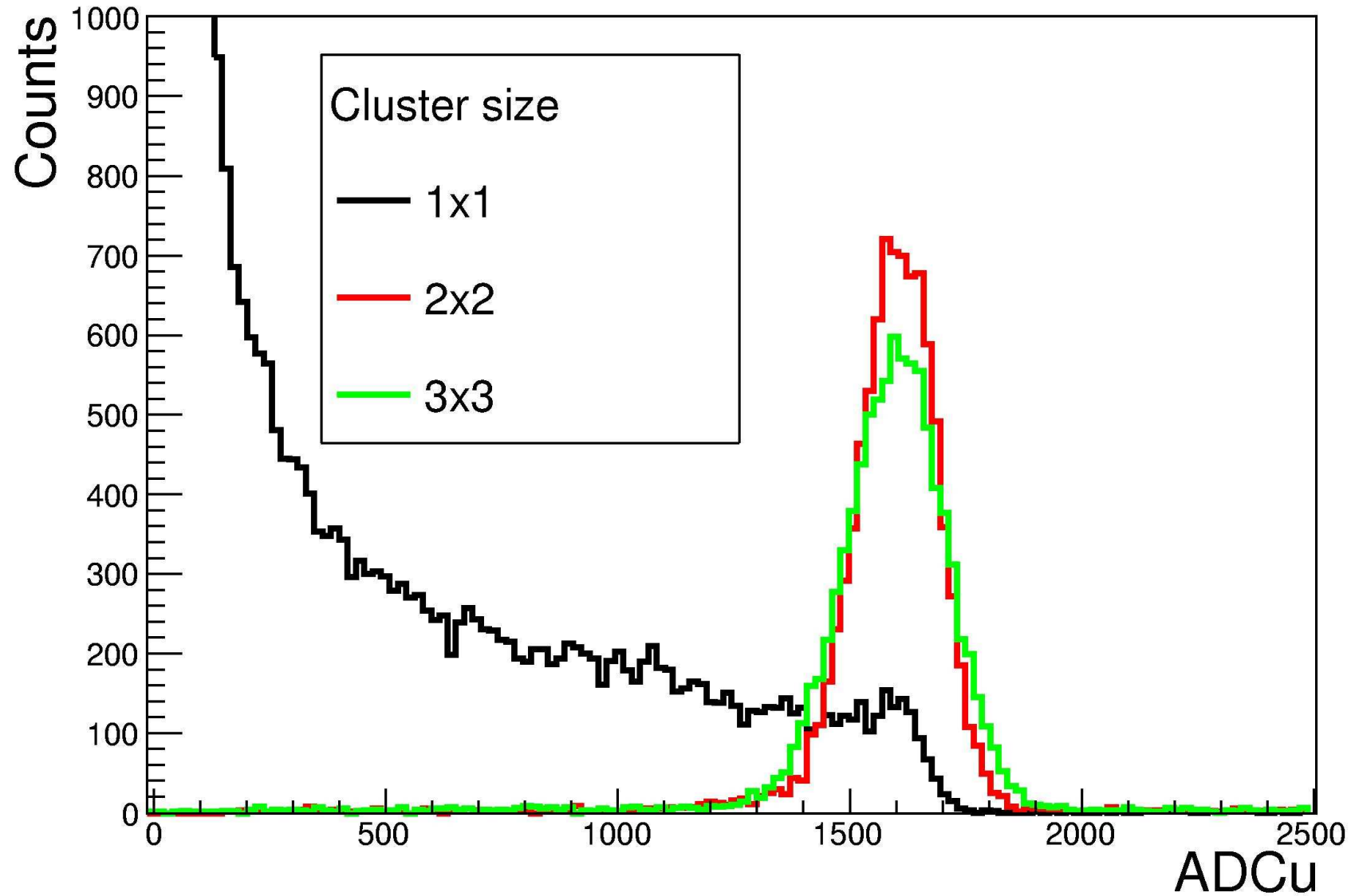
15um

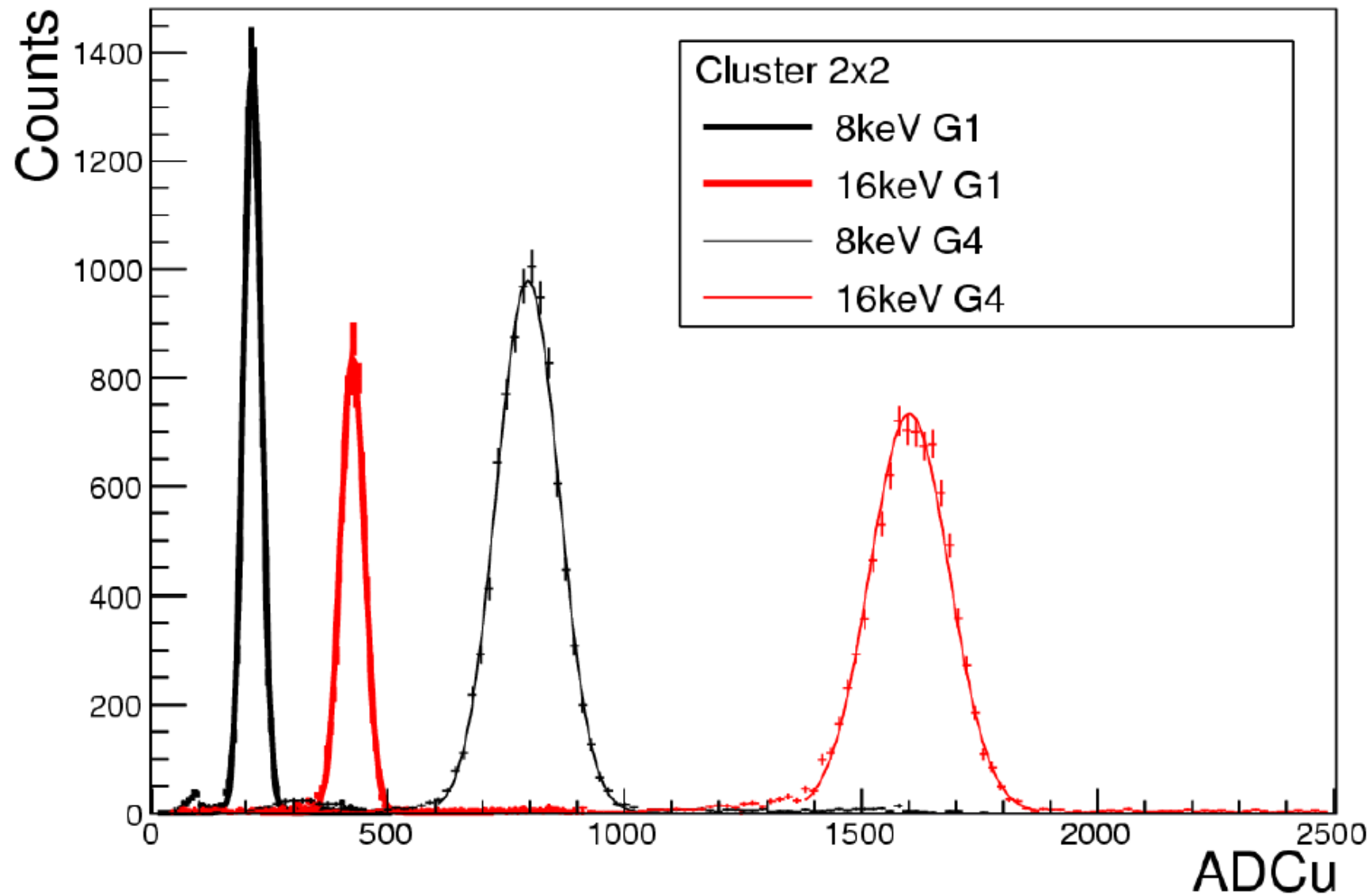
10um

Charge spread over multiple pixels
due to diffusion (charge sharing)

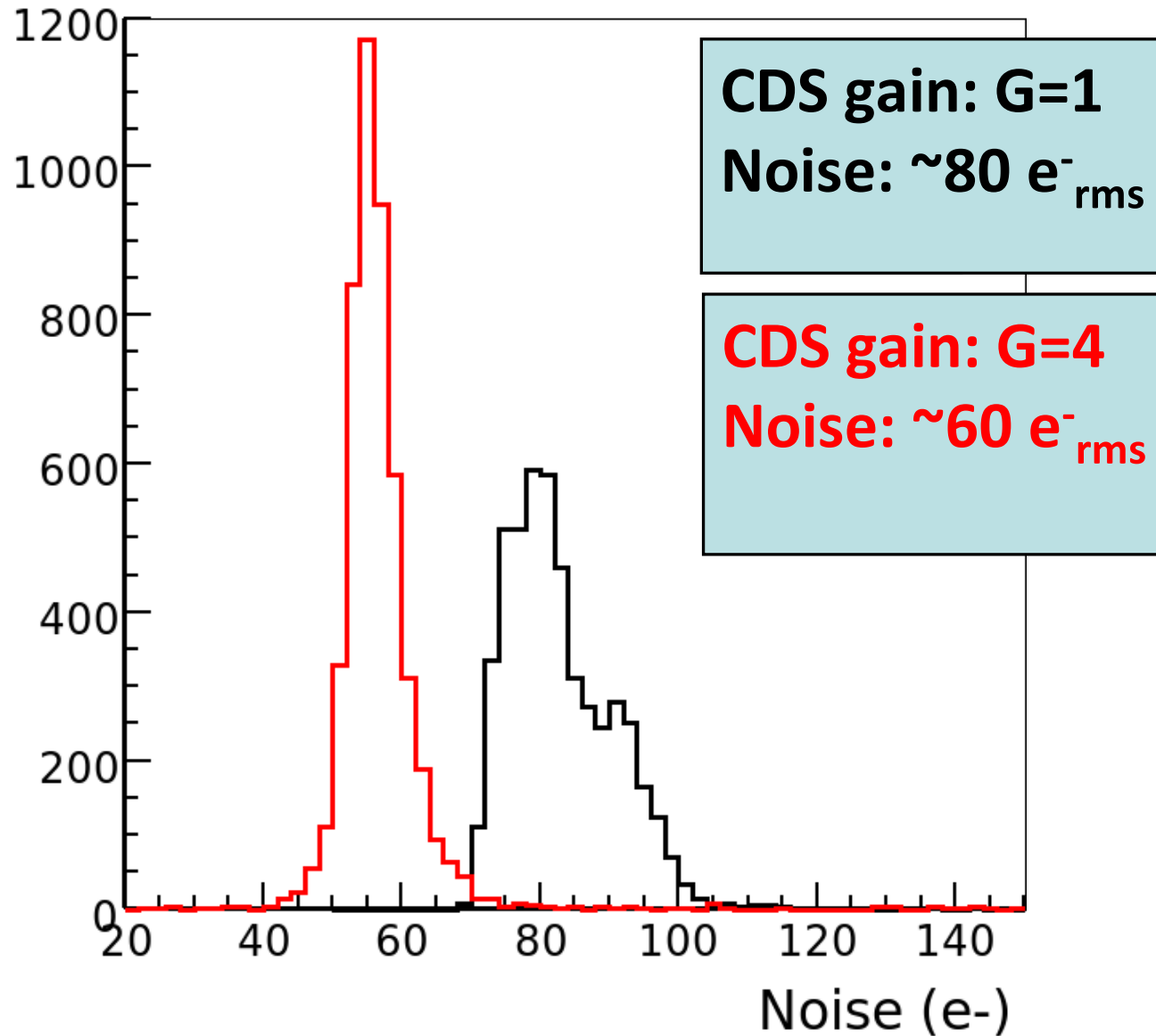
Charge distribution: 2d gauss
If the total signal of a cluster is above
noise floor \rightarrow hit detected







Main test results



CDS gain: G=1
Noise: ~80 e^-_{rms}

CDS gain: G=4
Noise: ~60 e^-_{rms}

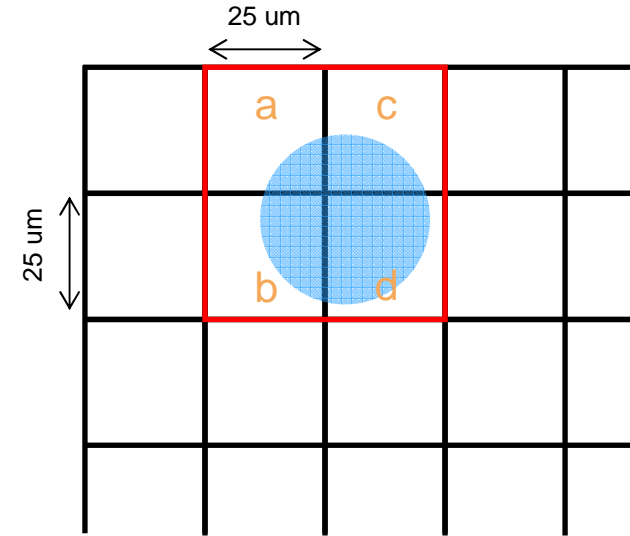
- **Block: S1**
- **Preamp gain: *static HIGH***

Interpolation in 2d

Linear position reconstruction

$$x_{sub} = \frac{a + b}{a + b + c + d}$$

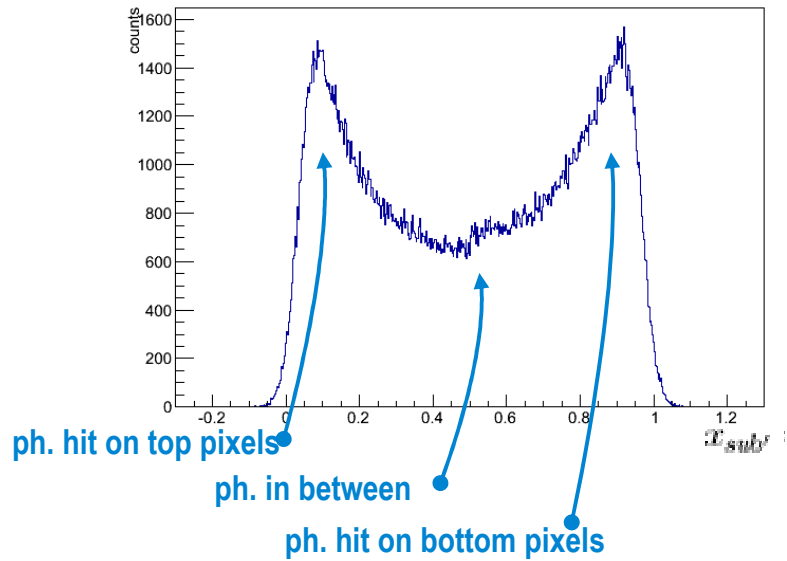
$$y_{sub} = \frac{a + c}{a + b + c + d}$$



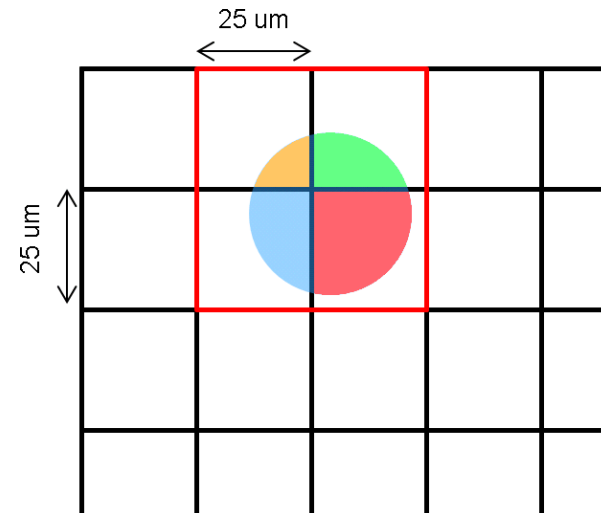
Eta algorithm

$$x_{sub} = \frac{\sum_{n=-\infty}^{x_{sub}} counts(n)}{tot.counts}$$

Position distribution of flat field data



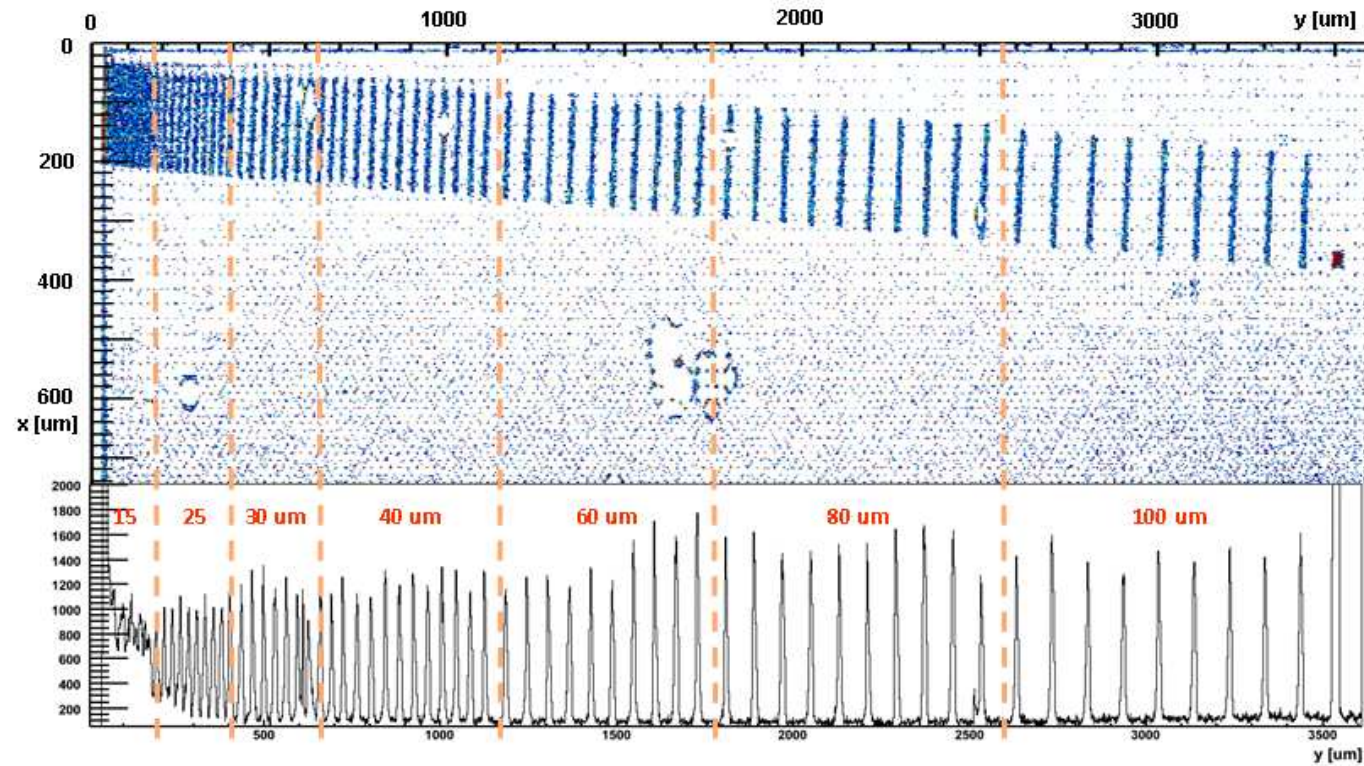
Circle area approach



It works

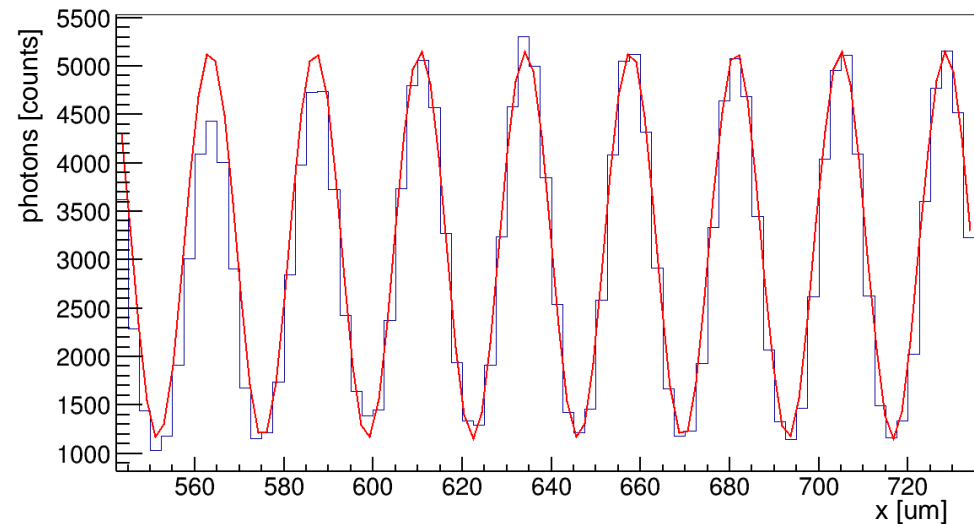
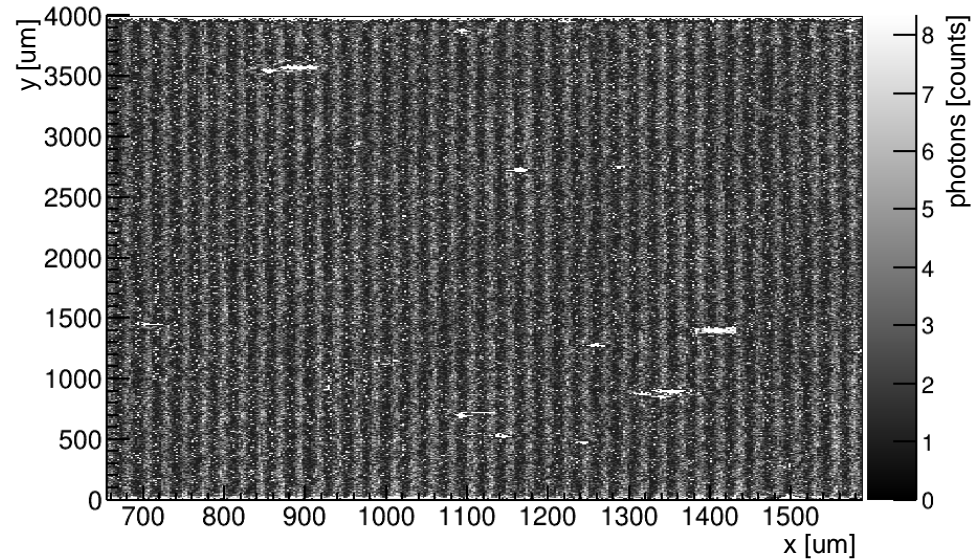
It gives a high contrast down to 12 μm

With interpolation (1 μm^2 bin size):

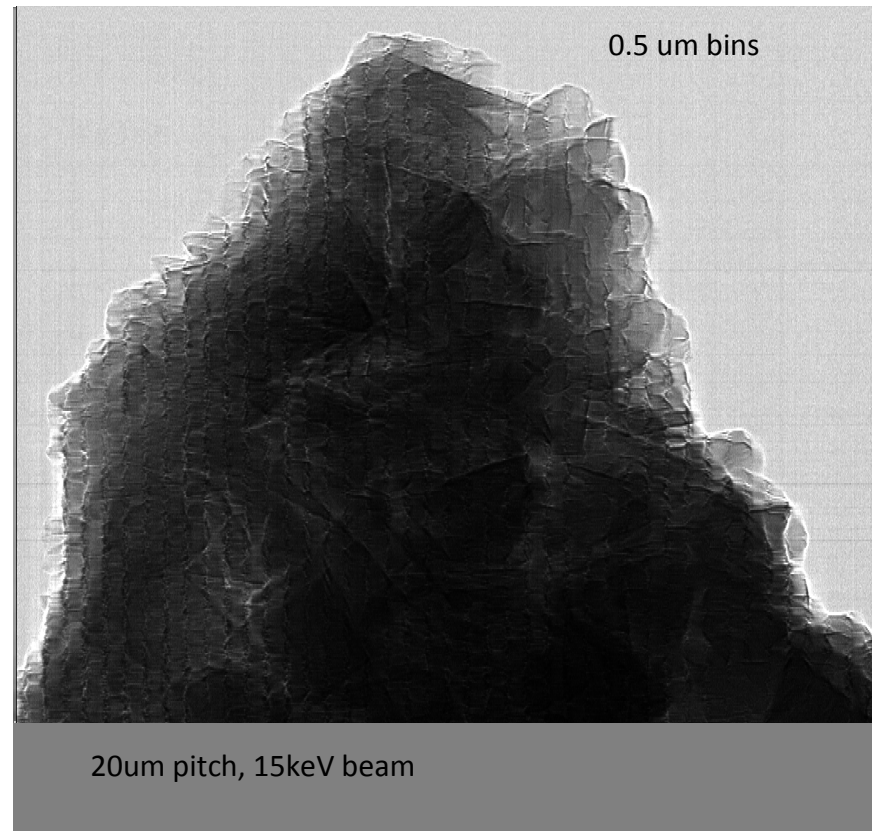
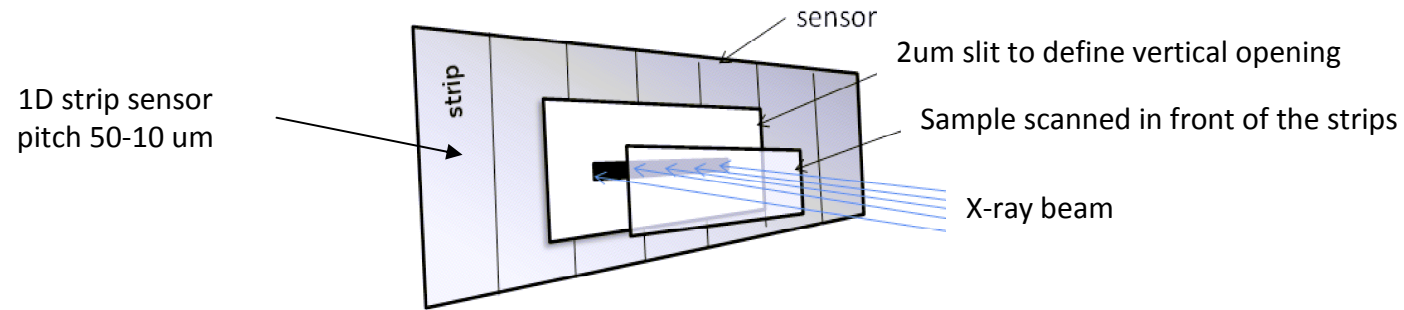


Period: 24.266 μm (23.571 μm measured)
 Gold: 50 μm thick (Absorption at 15keV $\sim 100\%$)

Contrast difference: 78 %

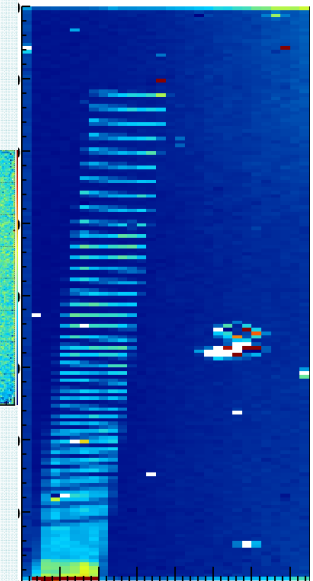
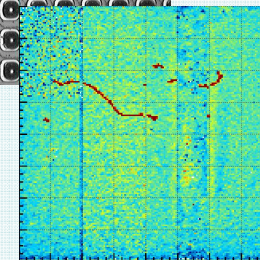
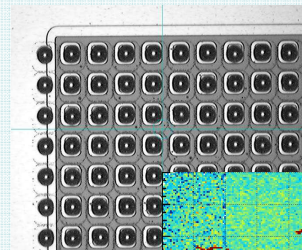
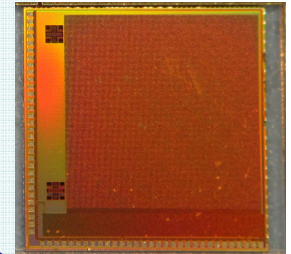


Results: Kidney Stone



Conclusions

- At PSI we are developing several new detectors based on charge integration and analog readout: GOTTHARD (1D); AGIPD, JUNGFRAU, MÖNCH (2D)
- MÖNCH is intended to investigate the limits of hybrid pixel detectors for X-ray detection.
- MONCH02 is a full chip containing an array of 160x160 pixels with pitch of 25µm. It contains several sub-blocks featuring different pixel archit.
- Preliminary results are encouraging:
 - We can bump-bond at 25 µm pitch
 - All blocks are functional
 - We can resolve small features
 - Noise is $\sim 60 e^-_{rms}$



A LOT still to be done: we are just at the beginning...

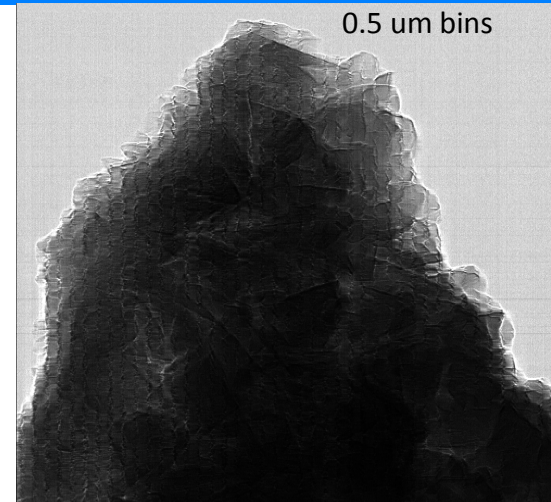
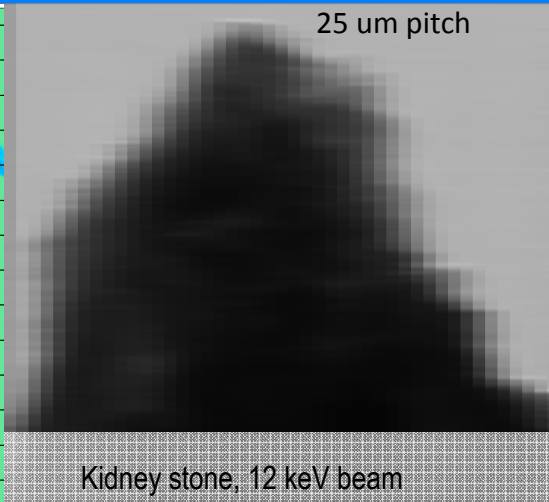
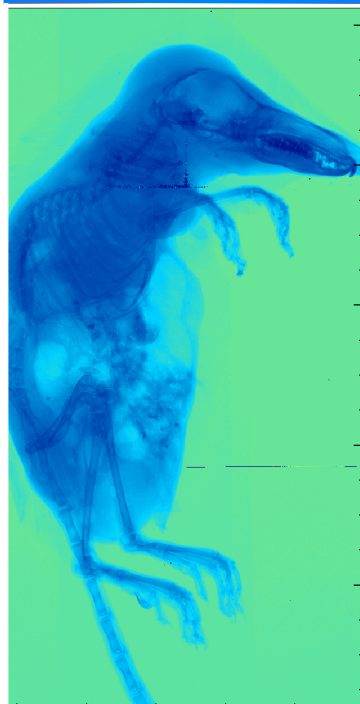
Many thanks to:

Anna Bergamaschi, Heiner Billich, Beat Henrich, Dominic Greiffenberg, Roland Horisberger, Ian Johnson, Dhanya Maliakal, Beat Meier, Aldo Mozzanica, Peter Oberta, Lukas Schaedler, Nick Schlumpf, Elmar Schmid, Bernd Schmitt, Xintian Shi, Akos Schreiber, Anja Schubert, Silvan Streuli, Dominic Suter, Valeria Radicci, Gerd Theidel.

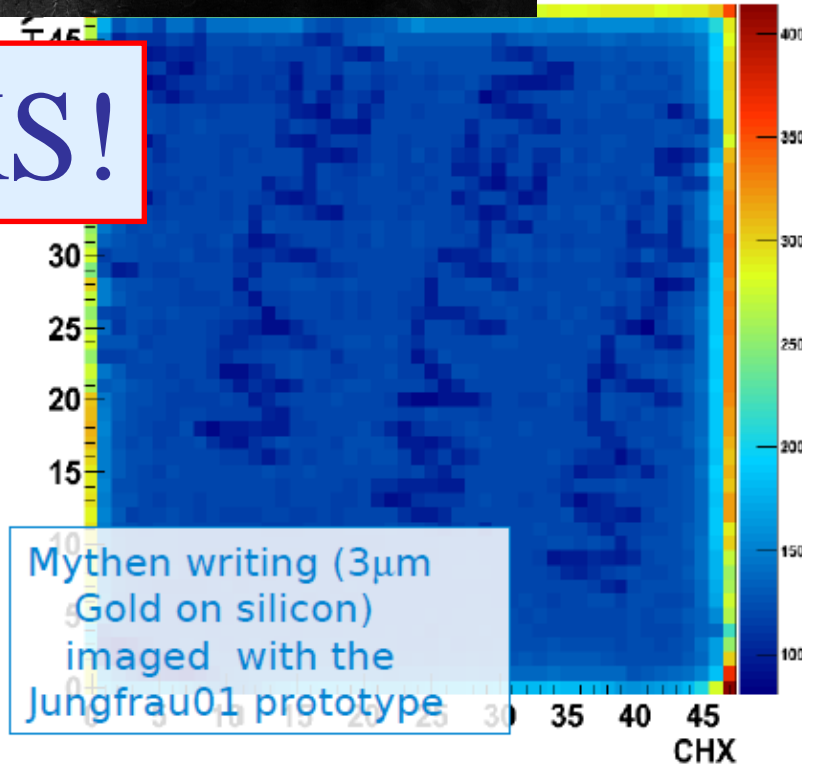
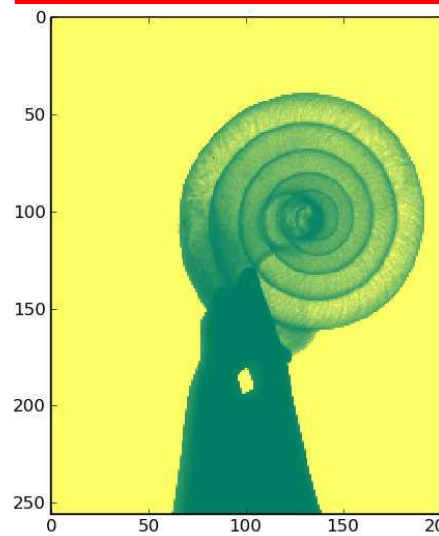
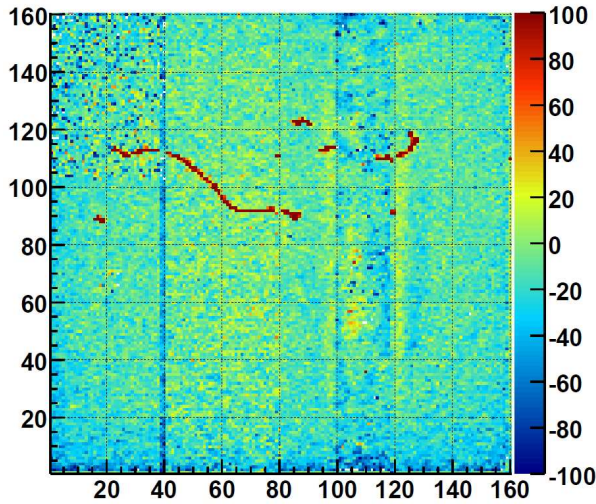
SLS Detector Group



**Not in the picture:
Julia Jungmann,
Davide Mezza**

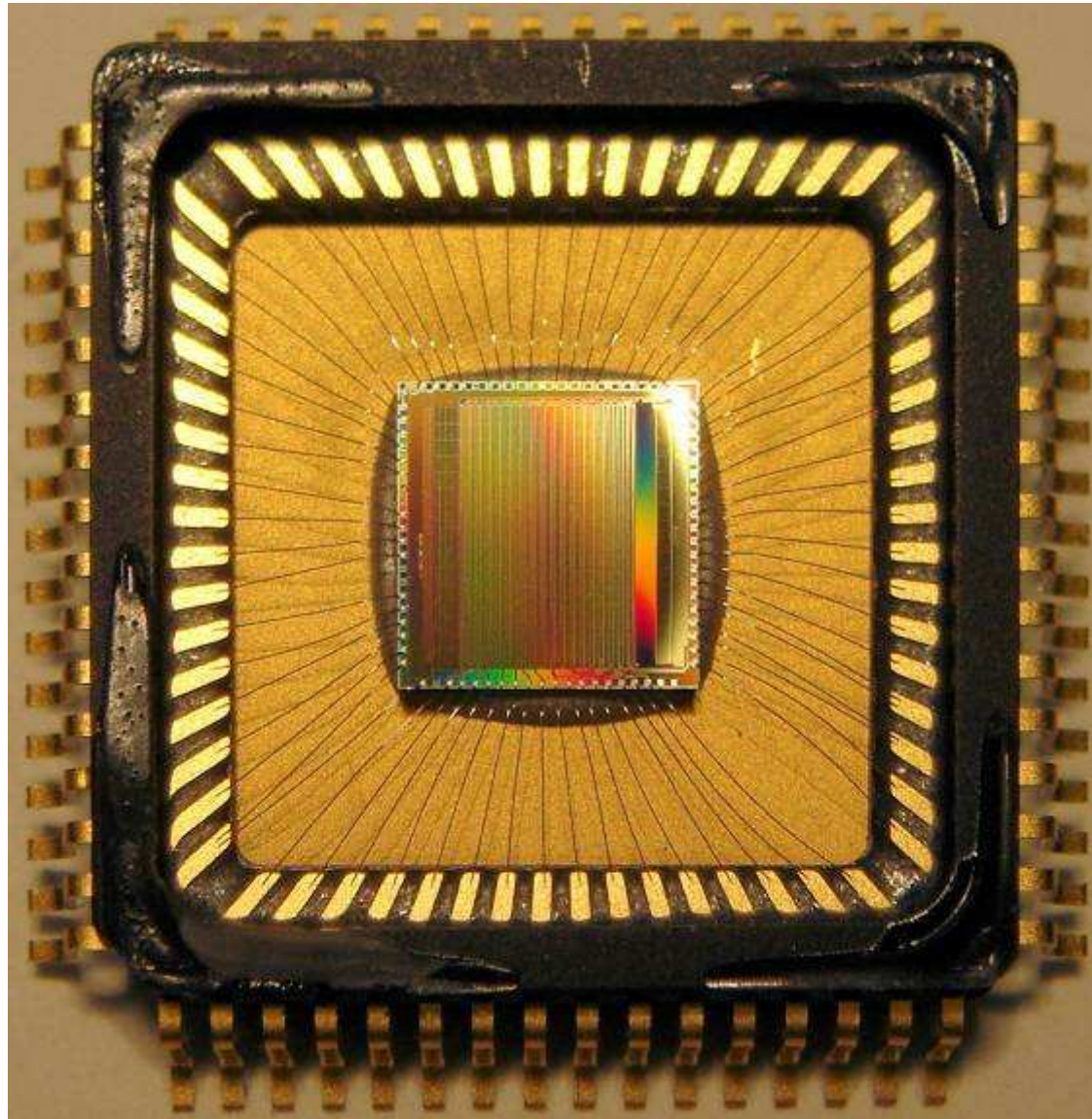


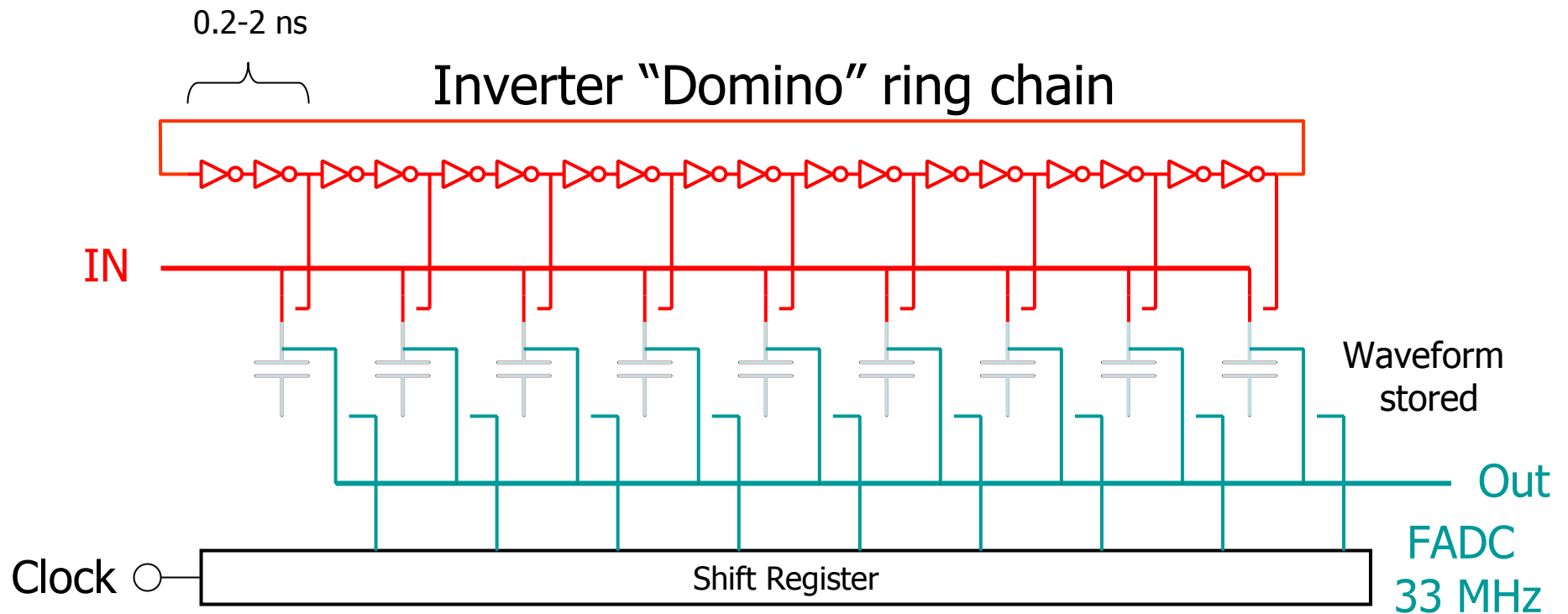
THANKS!



BACKUP SLIDES

5 GS/s Waveform Digitizing Chip DRS4 (S.Ritt, R.Dinapoli)

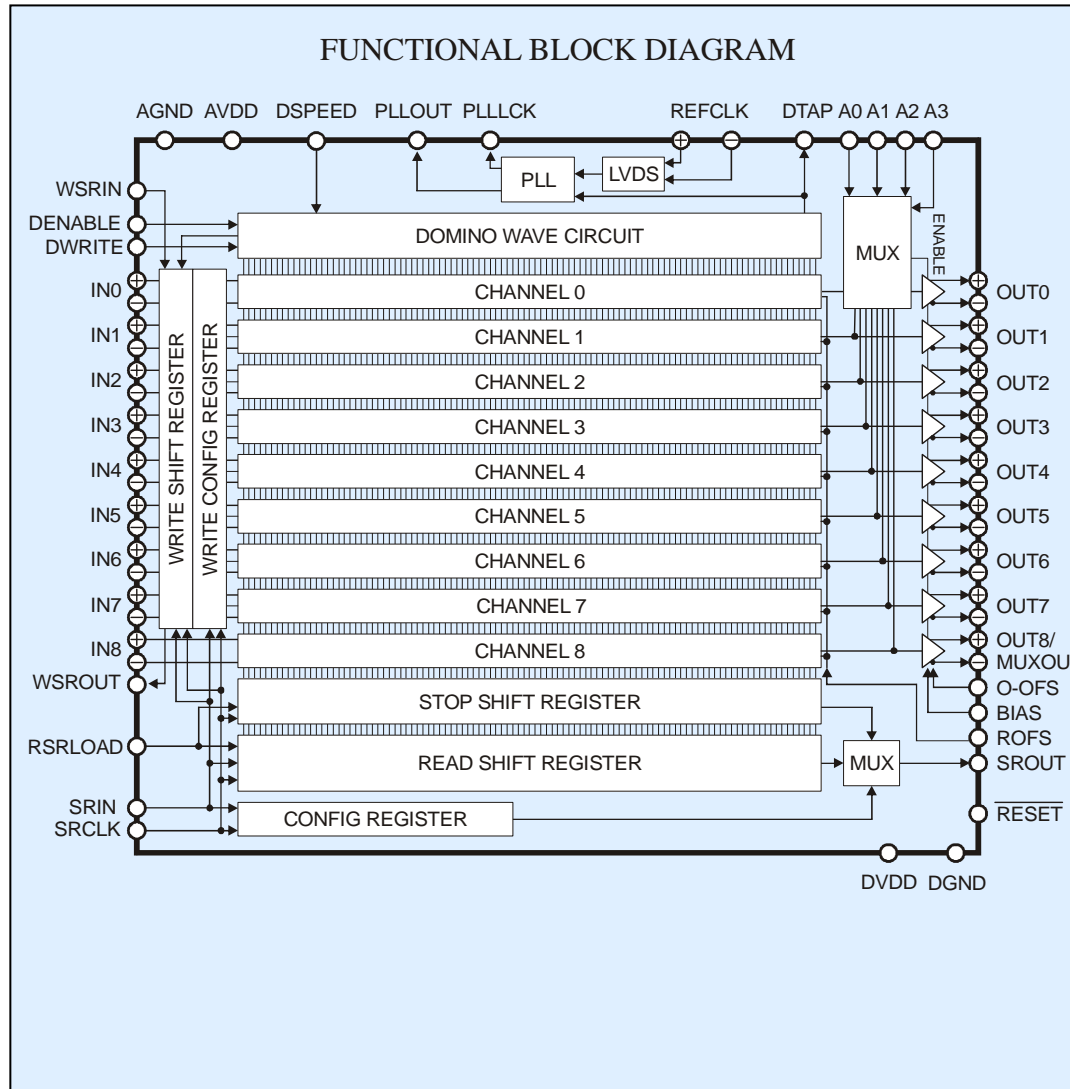




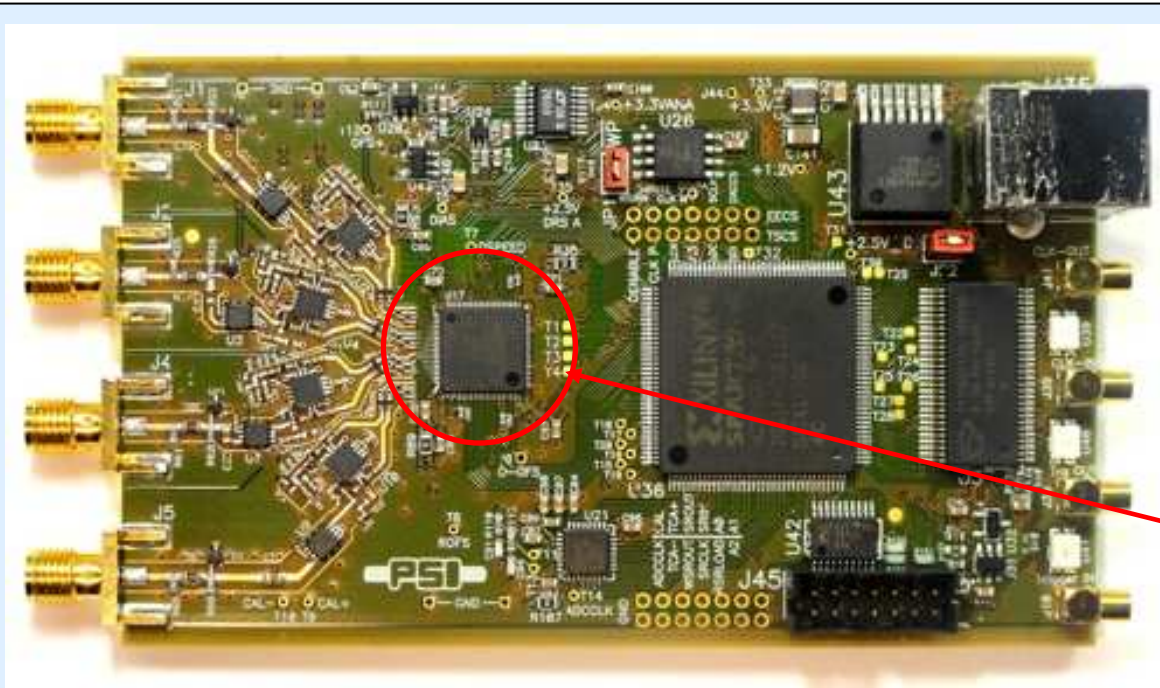
"Time stretcher" GHz → MHz

Keep Domino wave running in a circular fashion and stop by trigger → Domino Ring Sampler (DRS)

5 GS/s Waveform Digitizing Chip DRS4



- UMC 0.25 μm rad. hard
- 9 chn. each 1024 bins, cascadable up to 8192
- Sampling speed 0.2 ... 5 GS/s
- Bandwidth 950 MHz
- 17.5 mW/chn @ 2.5V
- On-chip PLL stabilization
- Readout speed using ext. ADC: $30 \text{ ns} * n_{\text{samples}}$
- SNR: 69 dB calibrated
- Aperture jitter: 4 ps at 5 GS/s calibrated



- Equivalent to 4-chn. 5 GS/s digital oscilloscope
- Board or packaged chip can be obtained from PSI

DRS4

Experiments using DRS chip

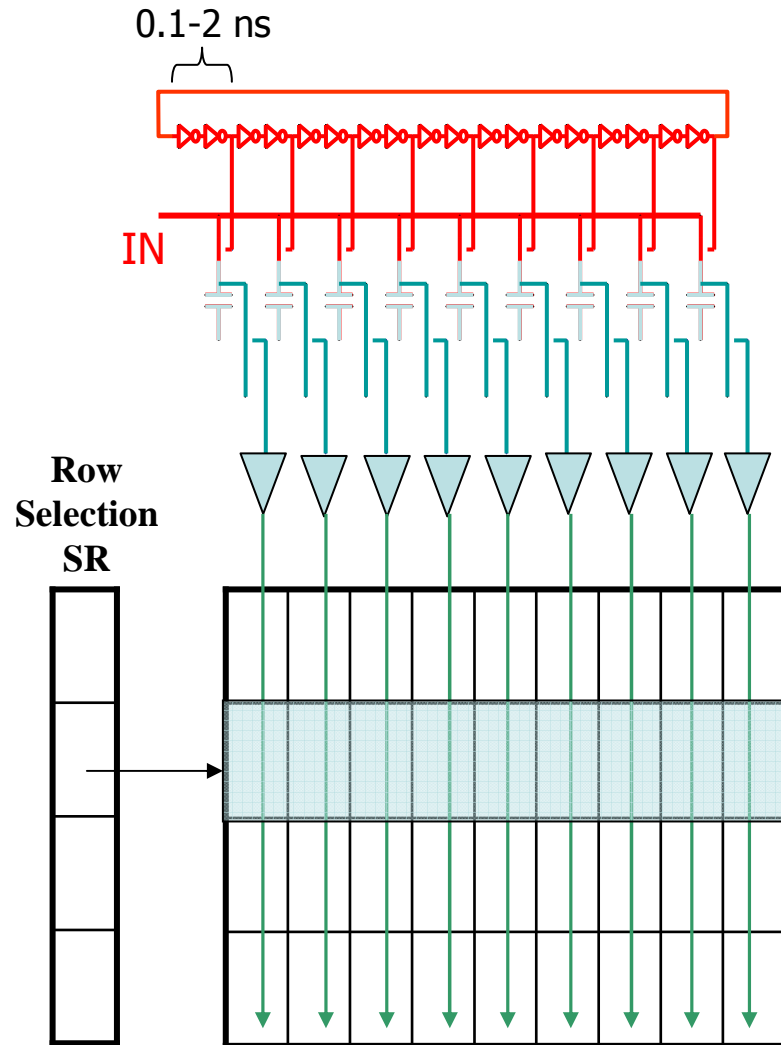
MEG 3000 channels DRS2

MACE (India) 1200 channels DRS4 (planned)

MAGIC-II 400 channels DRS2

BPM for XFEL@PSI 800 channels DRS4

Several CAEN products (e.g.: V1742 VME Switched Capacitor Digitizer) are based on DRS4



Shorter "Domino" ring chain

Short term storage

Fast buffers

Long term storage

Next step: DRS5

